

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT8353953

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
CROCUS TECHNOLOGY INC.	12/21/2023

RECEIVING PARTY DATA

Name:	CROCUS TECHNOLOGY SA
Street Address:	3 AVENUE DOYEN LOUIS WEIL
Internal Address:	6ÈME ÉTAGE
City:	GRENOBLE
State/Country:	FRANCE
Postal Code:	38000

PROPERTY NUMBERS Total: 63

Property Type	Number
Patent Number:	7986548
Patent Number:	8014192
Patent Number:	7251156
Patent Number:	7539045
Patent Number:	7485976
Patent Number:	7224634
Patent Number:	7568082
Patent Number:	7685438
Patent Number:	7277317
Patent Number:	7382664
Patent Number:	7529987
Patent Number:	7263018
Patent Number:	7474547
Patent Number:	7468905
Patent Number:	7359233
Patent Number:	7355882
Patent Number:	7286392
Patent Number:	7382642
Patent Number:	7733729

PATENT

Property Type	Number
Patent Number:	8120003
Patent Number:	7791920
Patent Number:	8273582
Patent Number:	8261367
Patent Number:	8134881
Patent Number:	8962493
Patent Number:	8625336
Patent Number:	8467234
Patent Number:	8576615
Patent Number:	8488372
Patent Number:	8611140
Patent Number:	8611141
Patent Number:	8717794
Patent Number:	9218879
Patent Number:	8409880
Patent Number:	8587079
Patent Number:	8902643
Patent Number:	8933750
Patent Number:	9228855
Patent Number:	8652856
Patent Number:	9059400
Patent Number:	9054029
Patent Number:	9267816
Patent Number:	9395209
Patent Number:	9310223
Patent Number:	9395210
Patent Number:	9350359
Patent Number:	9503097
Patent Number:	9324937
Patent Number:	9728233
Patent Number:	9702944
Patent Number:	9720057
Patent Number:	9689936
Patent Number:	9766305
Patent Number:	10401442
Patent Number:	10345091
Patent Number:	9841266
Patent Number:	9866182

Property Type	Number
Patent Number:	10460779
Patent Number:	10914795
PCT Number:	IB2160743
PCT Number:	IB2259686
PCT Number:	IB2357466
PCT Number:	IB2357321

CORRESPONDENCE DATA

Fax Number: (781)401-9966
Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.
Phone: 7814019988
Email: jlm@dc-m.com
Correspondent Name: DALY, CROWLEY, MOFFORD & DURKEE, LLP
Address Line 1: ONE UNIVERSITY AVENUE
Address Line 2: SUITE 201B
Address Line 4: WESTWOOD, MASSACHUSETTS 02090

ATTORNEY DOCKET NUMBER:	CROCUS INC TO SA
NAME OF SUBMITTER:	PAUL D. DURKEE
SIGNATURE:	/Paul D. Durkee/
DATE SIGNED:	12/28/2023

Total Attachments: 14
source=TO_PTO_ASSIGNMENT_INC_TO_SA#page1.tif
source=TO_PTO_ASSIGNMENT_INC_TO_SA#page2.tif
source=TO_PTO_ASSIGNMENT_INC_TO_SA#page3.tif
source=TO_PTO_ASSIGNMENT_INC_TO_SA#page4.tif
source=TO_PTO_ASSIGNMENT_INC_TO_SA#page5.tif
source=TO_PTO_ASSIGNMENT_INC_TO_SA#page6.tif
source=TO_PTO_ASSIGNMENT_INC_TO_SA#page7.tif
source=TO_PTO_ASSIGNMENT_INC_TO_SA#page8.tif
source=TO_PTO_ASSIGNMENT_INC_TO_SA#page9.tif
source=TO_PTO_ASSIGNMENT_INC_TO_SA#page10.tif
source=TO_PTO_ASSIGNMENT_INC_TO_SA#page11.tif
source=TO_PTO_ASSIGNMENT_INC_TO_SA#page12.tif
source=TO_PTO_ASSIGNMENT_INC_TO_SA#page13.tif
source=TO_PTO_ASSIGNMENT_INC_TO_SA#page14.tif

ASSIGNMENT

Whereas CROCUS TECHNOLOGY INC., a corporation organized and existing under the laws of the State of Delaware, United States of America, having a registered office at 251 Little Falls Drive, Wilmington, New Castle, Delaware, 19808, United States of America, having its principal place of business at 870 North McCarthy Boulevard, Suite 220, Milpitas, California, 95035, United States of America, and registered under file number 4178662 (hereinafter "Assignor") may have the legal right, title and interest in and to certain intellectual property listed in the attached Schedule A attached hereto; and

Whereas CROCUS TECHNOLOGY, a French *Société anonyme*, having its registered office located at 3 Avenue Doyen Louis Weil 6ème étage, 38000 Grenoble, France, and registered under number 453 278 475 RCS Grenoble, France (hereinafter "Assignee") is desirous of acquiring the legal right, title and interest hereinafter recited;

Now, therefore, for valuable consideration furnished by Assignee to Assignor, the receipt and sufficiency of which is hereby acknowledged, Assignor does hereby, without reservation:

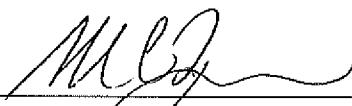
1. Assign, transfer and convey to Assignee the entire legal right, title and interest in and to said intellectual property listed in the attached Schedule A, any and all other applications for Letters Patent on said inventions and discoveries in whatsoever countries, including all divisional, renewal, substitute, continuation, reissue, PCT, re-examination and Convention applications based in whole or in part upon said inventions or discoveries, or upon said applications, and any and all Letters Patent, reissues, reexaminations, and extensions of Letters Patent granted for said inventions and discoveries or upon said applications, and every priority right that is or may be predicated upon or arise from said inventions, said discoveries, said applications and said Letters Patent and any provisional applications from which priority is claimed in said Letters Patent;
2. Authorize Assignee to file patent applications in any or all countries on any or all of said inventions and discoveries in the name of Assignee or otherwise as Assignee may deem advisable, under the International Convention or otherwise;
3. Authorize and request the Commissioner of Patents and Trademarks of the United States of America and the empowered officials of all other governments to issue or transfer all said Letters Patent to Assignee, as assignee of the entire legal right, title and interest therein or otherwise as Assignee may direct;
4. Assignor warrants that it has not knowingly conveyed to others any right in said inventions, discoveries, applications or patents or any license to use the same or to make, use or sell anything embodying or utilizing any of said inventions or discoveries; and that Assignor has good right to assign the same to Assignee without encumbrance;
5. Bind Assignor's legal representatives, assigns, and successors, as well as Assignor, to do, upon Assignee's request and at Assignee's expense, but without additional consideration to Assignor or such parties, all acts reasonably serving to assure that the said inventions and discoveries, the said patent applications and the said Letters Patent shall be held and enjoyed by Assignee as fully and entirely as the same could have been held and enjoyed by Assignor, legal

representatives, assigns, and successors if this assignment had not been made; and particularly to execute and deliver to Assignee all lawful application documents including petitions, specifications, and oaths, and all assignments, disclaimers, and lawful affidavits in form and substance as may be requested by Assignee; to communicate to Assignee all facts known to Assignor relating to said inventions and discoveries or the history thereof; and to furnish Assignee with any and all documents, photographs, models, samples and other physical exhibits in Assignor controls or in the control of Assignor's legal representatives, assigns, or successors which may be useful for establishing the facts of Assignor's conceptions, disclosures, and reduction to practice of said inventions and discoveries;

In testimony of which authorized representatives of CROCUS TECHNOLOGY INC and CROCUS TECHNOLOGY SA, have executed this Assignment as a binding instrument on the date indicated next to their respective names.

For CROCUS TECHNOLOGY INC.
a Delaware corporation


Dated: December 21, 2023



CROCUS TECHNOLOGY INC.
By: Michael Doogue
In his capacity as President of CROCUS
TECHNOLOGY INC.

For CROCUS TECHNOLOGY
a French *Société anonyme*

Dated: December 21, 2023



CROCUS TECHNOLOGY
By: Michael Doogue
In his capacity as President of CROCUS
TECHNOLOGY

Schedule A

Patents registered within the United States:

Country Code	Title	File Date	App Serial Number	Application Publication Number	Patent Document No.
US	MRAM reference cell with shape anisotropy to establish a well-defined magnetization orientation between a reference layer and a storage layer	2018-02-07	15/891233	US20180226112 A1	US10460779B2
US	Magnetic logic units configured to measure magnetic field direction	2014-11-24	14/552363	US20150077097 A1	US9395210B2
US	Apparatus and method for magnetic sensor based surface shape analysis	2015-09-18	14/859117	US20160084674 A1	US10345091B2
US	Magnetic field shaping conductor	2004-10-01	10/574365	US20070120209 A1	US7468905B2
US	Magnetic random access memory devices including shared heating straps	2011-09-21	13/239162	US20130070520 A1	US8611140B2
US	Integrity control for data stored in a non-volatile memory	2004-05-26	10/559174	US20060155882 A1	US7529987B2
US	Compensating a long read time of a memory device in data comparison and write operations	2004-07-12	10/565149	US20060248258 A1	US7263018B2
US	Apparatus and method for magnetic sensor output compensation based upon ambient temperature	2019-02-20	16/280480	US20200264242 A1	US10914795B2

US	Memory devices with series-interconnected magnetic random access memory cells	2011-02-08	13/023441	US20120201073 A1	US8625336B2
US	MRAM architecture for low power consumption and high selectivity	2004-01-14	10/543396	US20060087879 A1	US7277317B2
US	Apparatus, system, and method for matching patterns with an ultra fast check engine based on flash cells	2011-12-01	13/309475	US20120143554 A1	US9218879B2
US	Magnetic memory architecture with shared current line	2003-11-06	10/536292	US20060023490 A1	US7251156B2
US	Method and device for performing active field compensation during programming of a magnetoresistive memory device	2004-11-17	10/579933	US20070070686 A1	US7355882B2
US	Active shielding for a circuit comprising magnetically sensitive materials	2004-08-20	10/570174	US20060262585 A1	US7474547B2
US	Magnetic logic units configured as analog circuit building blocks	2015-01-27	14/606960	US20150214952 A1	US9350359B2
US	Method for use in making electronic devices having thin-film magnetic components	2009-07-09	12/500573	US20110008915 A1	US8273582B2
US	Magnetic random access memory devices including heating straps	2011-09-21	13/239168	US20130070521 A1	US8611141B2

US	Thermally assisted MRAM including magnetic tunnel junction and vacuum cavity	2015-03-24	14/666363		US9324937B1
US	Memory array including magnetic random access memory cells and oblique field lines	2012-08-10	13/572566	US20130037898 A1	US8587079B2
US	Apparatus and method for magnetic sensor based surface shape analysis spatial positioning in a uniform magnetic field	2015-09-23	14/863121	US20160097630 A1	US9841266B2
US	Magnetic random access memory devices including multi-bit cells	2011-06-10	13/158316	US20120314488 A1	US8488372B2
US	Method and device for protection of an MRAM device against tampering	2010-02-19	12/708874	US20100146641 A1	US8261367B2
US	Magnetic logic units configured to measure magnetic field direction	2013-03-06	13/787585	US20130241536 A1	US9228855B2
US	Magnetic logic units configured to measure magnetic field direction	2014-11-24	14/552302	US20150077095 A1	US9267816B2
US	Method for use in making electronic devices having thin-film magnetic components	2012-05-10	13/468936	US20120225499 A1	US8409880B2
US	Serial magnetic logic unit architecture	2015-06-05	14/732561	US20150357006 A1	US9728233B2
US	Thermally stable reference voltage generator for MRAM	2010-04-23	12/765897	US20100202231 A1	US8134881B2
US	Hardware security device for magnetic memory cells	2003-12-15	10/539795	US20060146597 A1	US7224634B2

US	Magnetic random access memory cells with isolating liners	2014-03-10	14/203362	US20140252516 A1	US9059400B2
US	Apparatus and method for layout of magnetic field sensing elements in sensors	2015-07-16	14/801800	US20160018482 A1	US9766305B2
US	Simultaneous reading from and writing to different memory cells	2004-03-17	10/549559	US20060209600 A1	US7382664B2
US	Apparatus, system, and method for writing multiple magnetic random access memory cells with a single field line	2012-10-09	13/648221	US20130094283 A1	US8902643B2
US	Current re-routing scheme for serial-programmed MRAM	2003-10-29	10/536271	US20060023489 A1	US7986548B2
US	Method and device for improved magnetic field generation during a write operation of a magnetoresistive memory device	2003-11-06	10/536293	US20060023491 A1	US7539045B2
US	MRAM-based pre-distortion linearization and amplification circuits	2016-05-13	15/154776	US20160336906 A1	US9866182B2
US	Non-homogeneous shielding of an MRAM chip with magnetic field sensor	2004-11-09	10/579929	US20070103967 A1	US7359233B2
US	Method and device for preventing erroneous programming of a magnetoresistive memory element	2004-11-09	10/579935	US20070153572 A1	US7382642B2
US	Magnetic logic units configured to measure	2014-11-24	14/552326	US20150077098 A1	US9395209B2

	magnetic field direction				
US	Apparatus and method for sensing a magnetic field using subarrays of magnetic sensing elements	2015-07-16	14/801797	US20160018481 A1	US9689936B2
US	Data retention indicator for magnetic memories	2004-11-09	10/579934	US20070165450 A1	US7286392B2
US	Magnetic random access memory devices including multi-bit cells	2011-06-10	13/158312	US20120314487 A1	US8576615B2
US	Magnetic logic units configured to measure magnetic field direction	2014-11-24	14/552338	US20150077096 A1	US9310223B2
US	Memory controller and method for writing to a memory	2003-11-26	10/541266	US20060106969 A1	US7568082B2
US	Magnetic random access memory devices configured for self-referenced read operation	2011-02-08	13/023442	US20120201074 A1	US8467234B2
US	Method and device to detect the likely onset of thermal relaxation in magnetic data storage devices	2003-11-06	10/536291	US20060018148 A1	US8014192B2
US	Active shielding for a circuit comprising magnetically sensitive materials	2008-11-25	12/277537	US20090073738 A1	US7791920B2
US	Apparatus and method for sensing a magnetic field using arrays of magnetic sensing elements	2015-07-16	14/801792	US20160018479 A1	US9702944B2

US	Method for use in making electronic devices having thin-film magnetic components	2013-03-21	13/848641	US20130288392 A1	US8652856B2
US	Tamper resistant packaging and approach	2003-12-15	10/538454	US20060108668 A1	US7485976B2
US	Nanowire magnetic random access memory	2006-09-22	12/088724	US20080251867 A1	US8120003B2
US	Apparatus and method for sensing a magnetic field using subarrays of magnetic field sensing elements for high voltage applications	2015-07-16	14/801794	US20160018480 A1	US9720057B2
US	Apparatus, system, and method for matching patterns with an ultra fast check engine	2011-12-01	13/309369	US20120143889 A1	US8717794B2
US	Analog circuits incorporating magnetic logic units	2015-01-27	14/606967	US20150214953 A1	US9503097B2
US	Thermally stable reference voltage generator for MRAM	2005-03-29	11/547336	US20080279027 A1	US7733729B2
US	Magnetic random access memory cells having improved size and shape characteristics	2010-12-13	12/966865	US20120146166 A1	US8962493B2
US	Magnetic logic units configured as an amplifier	2013-02-15	13/769156	US20130241636 A1	US8933750B2
US	Tamper-resistant packaging and approach using magnetically-set data	2004-01-14	10/541884	US20070139989 A1	US7685438B2

US	Memory devices with magnetic random access memory (MRAM) cells and associated structures for connecting the MRAM cells	2014-08-25	14/468234	US20140361392 A1	US9054029B2
US	Apparatus, system, and method for sensing communication signals with magnetic field sensing elements	2015-07-16	14/801802	US20160018483 A1	US10401442B2

Patents registered outside the United States:

Country	Title	Filing date	Application number	Published under N°
PCT	LINEARIZATION OF MAGNETIC SENSOR OUTPUT BASED ON CONTINUOUS CORRECTION OF HIGH ORDER VOLTAGE OUTPUT COMPONENTS		WO20211B60743	WO2022144621
PCT-EP	LINEARIZATION OF MAGNETIC SENSOR OUTPUT BASED ON CONTINUOUS CORRECTION OF HIGH ORDER VOLTAGE OUTPUT COMPONENTS			EP4272009
PCT-JP	LINEARIZATION OF MAGNETIC SENSOR OUTPUT BASED ON CONTINUOUS CORRECTION OF HIGH ORDER VOLTAGE OUTPUT COMPONENTS		JP20230540805	
PCT-KR	LINEARIZATION OF MAGNETIC SENSOR OUTPUT BASED ON CONTINUOUS CORRECTION OF HIGH ORDER VOLTAGE OUTPUT COMPONENTS		KR1020237025981	

PCT	Common Mode Field Rejection for Contactless Current Sensing		WO2022IB59686	WO2023079386
EP	Cascade magnetic sensor circuit and a linear magnetic sensor device comprising the cascade magnetic sensor circuit		EP20220315165	
PCT	Cascade magnetic sensor circuit and a linear magnetic sensor device comprising the cascade magnetic sensor circuit		WO2023IB57466	
EP	Correction method for correcting the output voltage of a magnetic sensor		EP20220315164	
PCT	Correction method for correcting the output voltage of a magnetic sensor		WO2023IB57421	
EP	DATA RETENTION INDICATOR FOR MAGNETIC MEMORIES	2004-11-09	EP20040799096	EP1690263B1
EP	MAGNETIC MEMORY WITH A THERMALLY ASSISTED WRITING PROCEDURE	2008-11-10	EP20080858835	EP2232495B1
EP	Magnetic random access memory with an elliptical junction	2009-05-13	EP20090160167	EP2124228B1
EP	SHARED LINE MAGNETIC RANDOM ACCESS MEMORY CELLS	2009-06-17	EP20090765872	EP2289102B1
EP	Ternary content addressable magnetoresistive random access memory cell	2009-11-03	EP20090174930	EP2204814B1
JP	Trivalent reluctance random access memory cell	2009-11-02	JP2009251996A	JP5674302B2

EP	Magnetic memory with a thermally assisted writing procedure and reduced writing field	2009-05-08	EP20090290339	EP2249349B1
EP	Magnetic memory with a thermally assisted spin transfer torque writing procedure using a low writing current	2009-05-08	EP20090290340	EP2249350B1
EP	Ultimate magnetic random access memory-based ternary CAM	2009-07-02	EP20090290534	EP2270812B1
EP	Magnetic element with a fast spin transfer torque writing procedure	2010-07-07	EP20100168719	EP2278589B1
EP	Circuit for generating adjustable timing signals for sensing a self-referenced MRAM cell	2010-09-10	EP20100176241	EP2309514B1
EP	MRAM-based memory device with rotated gate	2010-03-02	EP20100290102	EP2363862B1
EP	Multi level magnetic element	2011-10-24	EP20110186369	EP2447949B1
EP	MAGNETIC RANDOM ACCESS MEMORY CELLS HAVING IMPROVED SIZE AND SHAPE CHARACTERISTICS	2011-12-12	EP20110849075	EP2652739B1
EP	Multibit magnetic random access memory cell with improved read margin	2011-12-14	EP20110193528	EP2466586B1
EP	MAGNETIC RANDOM ACCESS MEMORY DEVICES CONFIGURED FOR SELF-REFERENCED READ OPERATION	2012-02-02	EP20120744425	EP2673779B1
EP	Magnetic random access memory cell with a dual junction for ternary content addressable memory applications	2012-03-16	EP20120159962	EP2506265B1
EP	MAGNETIC RANDOM ACCESS MEMORY DEVICES INCLUDING MULTI-BIT CELLS	2012-06-08	EP20120795992	EP2718927B1
EP	MEMORY ARRAY INCLUDING MAGNETIC RANDOM	2012-08-13	EP20120823484	EP2742507B1

	ACCESS MEMORY CELLS AND OBLIQUE FIELD LINES			
EP	MAGNETIC RANDOM ACCESS MEMORY DEVICES INCLUDING HEATING STRAPS	2012-09-19	EP20120833800	EP2758960B1
EP	APPARATUS, SYSTEM, AND METHOD FOR WRITING MULTIPLE MAGNETIC RANDOM ACCESS MEMORY CELLS WITH A SINGLE FIELD LINE	2012-10-09	EP20120839765	EP2766906B1
EP	MAGNETIC LOGIC UNITS CONFIGURED AS AN AMPLIFIER	2013-02-15	EP20130749177	EP2815401B1
EP	MAGNETIC LOGIC UNITS CONFIGURED TO MEASURE MAGNETIC FIELD DIRECTION	2013-03-06	EP20130757552	EP2823512B1
EP	MEMORY DEVICES WITH MAGNETIC RANDOM ACCESS MEMORY (MRAM) CELLS AND ASSOCIATED STRUCTURES FOR CONNECTING THE MRAM CELLS	2013-10-18	EP20130848270	EP2909837B1
EP	Self-referenced multibit MRAM cell having a synthetic antiferromagnetic storage layer	2014-06-17	EP20140290174	EP2958108B1
EP	MLU based accelerometer using a magnetic tunnel junction	2014-07-11	EP20140290201	EP2966453B1
BR	apparatus and method for the arrangement of magnetic field detection elements in sensors	2015-07-17	BR102015017209A	BR102015017209A2
EP	APPARATUS FOR MAGNETIC SENSOR BASED SURFACE SHAPE ANALYSIS	2015-09-18	EP20150841310	EP3194953B1
EP	Magnetic logic unit (MLU) cell for sensing magnetic fields with improved programmability and	2015-01-16	EP20150290014	EP3045928B1

	low reading consumption			
EP	MLU BASED MAGNETIC SENSOR HAVING IMPROVED PROGRAMMABILITY AND SENSITIVITY	2015-01-16	EP20150290013	EP3045927B1
EP	MAGNETIC DEVICE CONFIGURED TO PERFORM AN ANALOG ADDER CIRCUIT FUNCTION AND METHOD FOR OPERATING SUCH MAGNETIC DEVICE	2015-06-05	EP20150290151	EP3101654B1
EP	MAGNETORESISTIVE ELEMENT HAVING AN ADJUSTABLE MAGNETOSTRICTION AND MAGNETIC DEVICE COMPRISING THE MAGNETORESISTIVE ELEMENT	2016-03-10	EP20160290045	EP3217446B1
EP	MAGNETORESISTIVE-BASED SIGNAL SHAPING CIRCUIT FOR AUDIO APPLICATIONS	2016-03-16	EP20160290048	EP3220544B1
EP	MRAM REFERENCE CELL WITH SHAPE ANISOTROPY TO ESTABLISH A WELL-DEFINED MAGNETIZATION ORIENTATION	2018-02-07	EP20180750830	EP3580758A1
EP	MRAM REFERENCE CELL WITH SHAPE ANISOTROPY TO ESTABLISH A WELL-DEFINED MAGNETIZATION ORIENTATION	2018-02-07	EP20180750830	EP3580758A1
JP	Devices and methods for magnetic sensor output compensation based on ambient temperature	2020-02-19	JP2021547413A	JP2022520820A
KR	Apparatus and method for compensating magnetic sensor output based on ambient temperature	2020-02-19		KR20220009369A

PATENT

RECORDED: 12/28/2023

REEL: 066157 FRAME: 0645