

## PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1  
 Stylesheet Version v1.2

Assignment ID: PATI10266

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT	
<b>NATURE OF CONVEYANCE:</b>	RELEASE OF SECURITY INTEREST	
<b>CONVEYING PARTY DATA</b>		
<b>Name</b>		<b>Execution Date</b>
Citizens Bank, N.A.		02/02/2024
<b>RECEIVING PARTY DATA</b>		
<b>Company Name:</b>	Anokiwave, Inc.	
<b>Street Address:</b>	5355 Mira Sorrento Place	
<b>Internal Address:</b>	Suite 300	
<b>City:</b>	San Diego	
<b>State/Country:</b>	CALIFORNIA	
<b>Postal Code:</b>	92121	
<b>PROPERTY NUMBERS Total: 71</b>		
<b>Property Type</b>	<b>Number</b>	
Patent Number:	6903623	
Patent Number:	6924714	
Patent Number:	7915935	
Patent Number:	8260342	
Patent Number:	9154217	
Patent Number:	10224627	
Patent Number:	9455157	
Patent Number:	10320093	
Patent Number:	10862222	
Patent Number:	11133603	
Patent Number:	11011853	
Patent Number:	11349223	
Patent Number:	10290951	
Patent Number:	10587044	
Patent Number:	10382010	
Patent Number:	9876514	
Patent Number:	10263650	
Patent Number:	10559879	
Patent Number:	10200098	

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Property Type	Number
Patent Number:	10742288
Patent Number:	10826195
Patent Number:	10763226
Patent Number:	11177227
Patent Number:	10833391
Patent Number:	10355370
Patent Number:	10777888
Patent Number:	11081792
Patent Number:	11418971
Patent Number:	11063336
Patent Number:	10998640
Patent Number:	11296426
Patent Number:	10608756
Patent Number:	10985819
Patent Number:	11205858
Patent Number:	10855383
Patent Number:	11205846
Patent Number:	11114990
Patent Number:	11296860
Patent Number:	11082079
Patent Number:	11356167
Patent Number:	11411307
Patent Number:	11502419
Application Number:	17483230
Application Number:	17068376
Application Number:	17363829
Application Number:	17369557
Application Number:	17688318
Application Number:	17233074
Application Number:	17542082
Application Number:	17556195
Application Number:	17339677
Application Number:	17466632
Application Number:	17682700
Application Number:	17110806
Application Number:	17097860
Application Number:	17683928
Application Number:	17683916

Property Type	Number
Application Number:	17716359
Application Number:	17716625
Application Number:	63338714
Application Number:	17737567
Application Number:	17741907
Application Number:	63298748
Application Number:	63298753
Application Number:	63298756
Application Number:	63298758
Application Number:	17709271
Application Number:	17902219
Application Number:	63173120
Application Number:	63428976
Application Number:	63428890

#### CORRESPONDENCE DATA

##### Fax Number:

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Address Line 4: Washington, DISTRICT OF COLUMBIA 20036

ATTORNEY DOCKET NUMBER: 2263978 PAT

NAME OF SUBMITTER: Andrew Hackett

SIGNATURE: Andrew Hackett

DATE SIGNED: 02/07/2024

#### Total Attachments: 8

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**RELEASE AND TERMINATION OF INTELLECTUAL PROPERTY SECURITY AGREEMENT**

This RELEASE AND TERMINATION OF INTELLECTUAL PROPERTY SECURITY AGREEMENT, dated as of February 2, 2024 (this “Release”) is made by CITIZENS BANK, N.A., as lender (in such capacity, the “Lender”) in favor of ANOKIWAVE, INC., a Delaware corporation (together with its successors and assigns, the “Grantor”).

1.

Pursuant to the Security Agreement, dated as of December 8, 2022 (as amended, restated, supplemented or otherwise modified from time to time, the “Security Agreement”), entered into by the Grantor and the Lender, the Grantor and the Lender entered into that certain Intellectual Property Security Agreement, dated as of December 8, 2022 (as amended, restated, supplemented or otherwise modified from time to time prior to the date hereof, the “Intellectual Property Security Agreement”), and pursuant to the Security Agreement and the Intellectual Property Security Agreement, the Grantor granted to the Lender a lien on and security interest in, all of its right, title and interest in, to and under the IP Collateral (as defined in the Intellectual Property Security Agreement), including the IP Collateral set forth on Schedule A hereto and incorporated herein by reference.

2.

The Lender, without representation, warranty or recourse of any sort, hereby terminates, releases and discharges the grant of and all of its lien on and security interest in, all right, title and interest in, to and under the IP Collateral granted under the Intellectual Property Security Agreement, including the IP Collateral identified on Schedule A hereto and incorporated herein by reference. Any right, title or interest of the Lender in, to or under the IP Collateral shall hereby cease and become void. This Release does not terminate, release or discharge the Lender’s security interest in any intellectual property or any other asset of the Grantor other than the IP Collateral.

3.

The Lender hereby agrees to duly execute, acknowledge, procure and deliver any further documents and to do such other acts as may be reasonably necessary and requested by Grantor to effect the release of the security interest contemplated hereby, at Grantor’s expense. The Lender hereby authorizes the Grantor or the Grantor’s authorized representative to record this Release with the United States Patent and Trademark Office.

4.

This Release may be executed in any number of counterparts, each of which when so executed shall be deemed to be an original and all of which taken together shall constitute one and the same agreement. Delivery by facsimile or by electronic .pdf copy of an executed counterpart of a signature page to this Release shall be effective as delivery of an original executed counterpart of this Release.

5.

This Release shall be governed by, and construed in accordance with, the laws of the State of New York.

*(Signature page follows)*

IN WITNESS WHEREOF, the Lender has caused this document to be executed on its behalf as of the date first written above.

**CITIZENS BANK, N.A.**

as Lender

By: 

Name: Erin Kane

Title: Assistant Vice President

Address: 28 State Street  
Boston, Massachusetts 02109

**Schedule A**

**Released IP Collateral**

**TRADEMARK REGISTRATIONS**

<b>Serial Number</b>	<b>Registration Number</b>	<b>Country</b>	<b>Filing/Registration Dates</b>	<b>Mark</b>
87668317	5486259	US	Filed: 11/01/2017 Registered: 06/05/2018	ANOKIWAVE
88186606	5735855	US	Filed: 11/08/2018 Registered: 04/23/2019	INTELLIGENT GAIN BLOCK
88231866	6175823	US	Filed: 12/17/2018 Registered: 10/13/2020	ZERO-CAL
90235828	6396124	US	Filed: 10/05/2020 Registered: 06/22/2021	MMW SOLUTIONS. ENABLING A NEW WORLD
90235838	6756043	US	Filed: 10/05/2020 Registered: 06/07/2022	INTELLIGENT ARRAY IC SOLUTIONS
90235841	6654852	US	Filed: 10/05/2020 Registered: 02/22/2022	MMW ALGORITHMS TO ANTENNAS

**TRADEMARK APPLICATIONS**

<b>Serial Number</b>	<b>Country</b>	<b>Filing Date</b>	<b>Mark</b>
90560441	US	Filed: 03/04/2021	HANDSET TECHNOLOGY FOR INFRASTRUCTURE
90874444	US	Filed: 08/10/2021	WIFI TECHNOLOGY FOR INFRASTRUCTURE

**PATENT REGISTRATIONS**

<b>Filing Number</b>	<b>Patent No.</b>	<b>Country</b>	<b>Filing/Issuance Dates</b>	<b>Patent Title</b>
10/458,538	6,903,623	US	Filed: 06/09/2003 Issued: 06/07/2005	High isolation switch in balanced lines
10/438,723	6,924,714	US	Filed: 05/14/2003 Issued: 08/02/2005	High power termination for radio frequency (RF) circuits
12/618,109	7,915,935	US	Filed: 11/13/2009 Issued: 03/29/2011	Communication Systems With Counter-Based Frequency Centering For Mm-Wave Frequency Bands
12/683,354	8,260,342	US	Filed: 01/06/2010 Issued: 09/04/2012	Communications systems management using MM-wave based motion sensing
14/066,634	9,154,217	US	Filed: 10/29/2013 Issued: 10/06/2015	Direct conversion remote satellite communications terminal
14/967,092	10,224,627	US	Filed: 12/11/2015 Issued: 03/05/2019	Electronically scanned antenna arrays with reconfigurable performance
14/846,092	9,455,157	US	Filed: 09/04/2015 Issued: 09/27/2016	Method and Apparatus for Mitigating Parasitic Coupling in a Packaged Integrated Circuit
15/253,426	10,320,093	US	Filed: 08/31/2016 Issued: 06/11/2019	Phased Array Control Circuit
16/436,116	10,862,222	US	Filed: 06/10/2019 Issued: 12/08/2020	Phased Array Control Circuit
17/099,238	11,133,603	US	Filed: 11/16/2020 Issued: 09/28/2021	Phased Array Control Circuit for Controlling the Effective Shape of a Beam-Formed Signal
15/267,704	11,011,853	US	Filed: 09/16/2016 Issued: 05/18/2021	Laminar Phased Array with Polarization-Isolated Transmit/Receive Interfaces
17/232,283	11,349,223	US	Filed: 04/16/2021 Issued: 05/31/2022	Laminar Phased Array with Polarization-Isolated Transmit/Receive Interfaces
15/669,339	10,290,951	US	Filed: 08/04/2017 Issued: 05/14/2019	Hybrid Laminated Phased Array
15/792,484	10,587,044	US	Filed: 10/24/2017 Issued: 03/10/2020	Flip-chip beamforming integrated circuit with integral thermal mass
15/475,246	10,382,010	US	Filed: 03/31/2017 Issued: 08/13/2019	Attenuation Circuit and Method of Controlling an Attenuation Circuit
15/253,384	9,876,514	US	Filed: 08/31/2016 Issued: 01/23/2018	Calibration of active electronically steered antennas using on-chip programming
15/860,508	10,263,650	US	Filed: 01/02/2018 Issued: 04/16/2019	Calibration Of Active Electronically Steered Antennas Using On-Chip Programming

<b>Filing Number</b>	<b>Patent No.</b>	<b>Country</b>	<b>Filing/Issuance Dates</b>	<b>Patent Title</b>
15/212,813	10,559,879	US	Filed: 07/18/2016 Issued: 02/11/2020	Phased Array Burst Sampler
15/389,675	10,200,098	US	Filed: 12/23/2016 Issued: 02/05/2019	Phased array with beamforming integrated circuit having two signal chains
16/221,057	10,742,288	US	Filed: 12/14/2018 Issued: 08/11/2020	Phased array with beamforming integrated circuit having two signal chains
15/938,647	10,826,195	US	Filed: 03/28/2018 Issued: 11/03/2020	Apparatus and method for RF Isolation in a packaged integrated circuit
16/153,528	10,763,226	US	Filed: 10/05/2018 Issued: 09/01/2020	Method and Apparatus for Heat Sinking High Frequency IC With Absorbing Material
16/886,370	11,177,227	US	Filed: 05/28/2020 Issued: 11/16/2021	Method and Apparatus for Heat Sinking High Frequency IC With Absorbing Material
16/153,556	10,833,391	US	Filed: 10/05/2018 Issued: 11/10/2020	Thermal Management Method and Apparatus for High Frequency IC With Apertured Heat Sink
15/669,575	10,355,370	US	Filed: 08/04/2017 Issued: 07/16/2019	Dual Phased Array With Single Polarity Beam Steering Integrated Circuits
15/792,479	10,777,888	US	Filed: 10/24/2017 Issued: 09/15/2020	Beamforming Integrated Circuit With RF Grounded Material Ring
16/501,456	11,081,792	US	Filed: 03/07/19 Issued: 08/03/2021	Phased Array With Low-Latency Control Interface
16/231,799	11,418,971	US	Filed: 12/24/2018 Issued: 08/16/2022	Beamforming Integrated Circuit, AESA System and Method
16/375,251	11,063,336	US	Filed: 04/04/2019 Issued: 07/13/2021	Phased Array Architecture with Distributed Temperature Compensation and Integrated Up/Down Conversion
16/413,355	10,998,640	US	Filed: 05/15/2019 Issued: 05/04/2021	Cross-polarized Time Division Duplexed Antenna
17/226,887	11,296,426	US	Filed: 04/09/2021 Issued: 04/05/2022	Cross-Polarized Time Division Duplexed Antenna
16/122,680	10,608,756	US	Filed: 09/05/2018 Issued: 03/31/2020	Power Detector Calibration In Integrated Circuits
16/653,334	10,985,819	US	Filed: 10/15/2019 Issued: 04/20/2021	Element-Level Self-Calculation of Phased Array Vectors Using Interpolation
16/653,348	11,205,858	US	Filed: 10/15/2019 Issued: 12/21/2021	Element-Level Self-Calculation of Phased Array Vectors Using Direct Calculation



<b>Filing Number</b>	<b>Patent No.</b>	<b>Country</b>	<b>Filing/Issuance Dates</b>	<b>Patent Title</b>
16/353,893	10,855,383	US	Filed: 03/14/2019 Issued: 12/01/2020	Calibration Of Active Electronically Steered Antennas Using Trim Bits And Non-Volatile Memory
16/986,846	11,205,846	US	Filed: 08/06/2020 Issued: 12/21/2021	Beamforming Integrated Circuit Having RF Signal Ports Using a Ground-Signal Transition for High Isolation in a Phased Antenna Array System and Related Methods
16/991,673	11,114,990	US	Filed: 08/12/2020 Issued: 09/07/2021	Apparatus And Method Of Power Management Using Envelope Stacking
16/932,187	11,296,860	US	Filed: 07/17/2020 Issued: 04/05/2022	Phase-Aligning Multiple Synthesizers
16/987,344	11,082,079	US	Filed: 08/06/2020 Issued: 08/03/2021	Spur Mitigation In A Heterodyne Upconversion System
17/230,531	11,356,167	US	Filed: 04/14/2021 Issued: 06/07/2022	Selective calibration of signal processing integrated circuits in a phased array system
17/502,426	11,411,307	US	Filed: 10/15/2021 Issued: 08/09/2022	Pinwheel Three-Way Wilkinson Power Divider for Millimeter Wave Applications
17/100,023	11,502,419	US	Filed: 11/20/2020 Issued: 11/15/2022	Standard Printed Circuit Board Patch Array

### **PATENT APPLICATIONS**

<b>Filing Number</b>	<b>Country</b>	<b>Filing Date</b>	<b>Patent Title</b>
17/483,230	US	Filed: 09/23/2021	Phased Array Control Circuit for Controlling Directivity of a Beam-Formed Signal
17/068,376	US	Filed: 10/12/2020	Thermal Management Method and Apparatus for High Frequency IC with Apertured Heat Sink
17/363,829	US	Filed: 06/30/2021	Phased Array with Low-Latency Control Interface
17/369,557	US	Filed: 07/07/2021	Phased Array Architecture with Distributed Temperature Compensation and Integrated Up/Down Conversion
17/688,318	US	Filed: 03/07/2022	Cross-Polarized Time Division Duplexed Antenna
17/233,074	US	Filed: 04/16/2021	Fast Beam Steering Using Interpolation
17/542,082	US	Filed: 12/03/2021	Fast Beam Steering Using Direct Calculation
17/556,195	US	Filed: 12/20/2021	Beamforming Integrated Circuit Having RF Signal Ports Using a Ground-Signal Transition for High Isolation in a Phased Antenna Array System and Related Methods
17/339,677	US	Filed: 06/04/2021	Synthesizer with High-Q

<b>Filing Number</b>	<b>Country</b>	<b>Filing Date</b>	<b>Patent Title</b>
17/466,632	US	Filed: 09/03/2021	Apparatus and Method Of Power Management Using Envelope-Stacking
17/682,700	US	Filed: 02/28/2022	Phase-Aligning Multiple Synthesizers
17/110,806	US	Filed: 12/03/2020	Method and Apparatus for Fast Coarse Tuning in Synthesizers
17/097,860	US	Filed: 11/13/2020	Integrated Circuit and System with Tracking
17/683,928	US	Filed: 03/01/2022	Time-To-Digital Converter Using Voltage as a Representation of Time Offset
17/683,916	US	Filed: 03/01/2022	Time-to-Digital Converter and Calibration
17/716,359	US	Filed: 04/08/2022	Antenna and PCB Layout Topology Designs for Frequency Scalability in PCB Technology for Antenna Arrays
17/716,625	US	Filed: 04/08/2022	Array Lattice Techniques for High Symmetry and High Scan Performance
63/338,714	US	Filed: 05/05/2022	High-Isolation Package for Mitigating Coupling in a MM-Wave Packaged IC
17/737,567	US	Filed: 05/05/2022	Integrated Circuit Chips for a Phased Array System Including Programmable On-Chip Element Swapping, Channel Swapping, and/or Phase Rotation Circuitry
17/741,907	US	Filed: 05/11/2022	Joint Transmit/Receive Image Compensation with Blind Adaptive Calibration in a Transceiver System
63/298,748	US	Filed: 01/12/2022	Flexible Online Loft Compensation in RF Communication Systems and Devices
63/298,753	US	Filed: 01/12/2022	Flexible Calibration of Time-Varying Operating Parameters in RF Communication Systems and Devices Such as for Error Vector Control in Phase Array Systems and Devices
63/298,756	US	Filed: 01/12/2022	Array Self-Calibration
63/298,758	US	Filed: 01/12/2022	Integrated Single-Chip Beamforming System
17/709,271	US	Filed: 03/30/2022	Correction of Systematic Error for Electronically Steered Antennas Using On-Chip Programming
17/902,219	US	Filed: 09/02/2022	Standard Printed Circuit Board Patch
63/173,120	US	Filed: 04/09/2022	Array Lattice Techniques for High Symmetry and High Scan Performance
63/428,976	US	Filed: 11/30/2022	Phase Tracking Integrated Circuit
63/428,890	US	Filed: 11/30/2022	Hybrid Cascode Low-Noise Amplifier

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<b>Registration Number</b>	<b>Country</b>	<b>Registration Date</b>	<b>Title</b>
MW0000019746	US	Registered: 02/28/2018	Laguna.
MW0000019812	US	Registered: 02/28/2018	Odin.