

<b>PATENT ASSIGNMENT COVER SHEET</b>
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Assignment ID: PATI41467

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
Imperas Software Limited	02/01/2024
<b>RECEIVING PARTY DATA</b>	
<b>Company Name:</b>	Synopsys, Inc.
<b>Street Address:</b>	675 Almanor Avenue
<b>City:</b>	Sunnyvale
<b>State/Country:</b>	CALIFORNIA
<b>Postal Code:</b>	94085
<b>PROPERTY NUMBERS Total: 4</b>	
<b>Property Type</b>	<b>Number</b>
<b>Patent Number:</b>	8417508
<b>Patent Number:</b>	9658849
<b>Patent Number:</b>	11574087
<b>Application Number:</b>	18105554
<b>CORRESPONDENCE DATA</b>	
<b>Fax Number:</b>	6509385200
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
<b>Phone:</b>	650-988-8500
<b>Email:</b>	ldean@fenwick.com
<b>Correspondent Name:</b>	FENWICK & WEST LLP
<b>Address Line 1:</b>	801 California Street
<b>Address Line 4:</b>	Mountain View, CALIFORNIA 94041
<b>ATTORNEY DOCKET NUMBER:</b>	22524-01000
<b>NAME OF SUBMITTER:</b>	Laura Dean
<b>SIGNATURE:</b>	Laura Dean
<b>DATE SIGNED:</b>	02/21/2024
<b>Total Attachments: 2</b>	
source=01000 Executed Assignment from Imperas to Synopsys#page1.tif	
source=01000 Executed Assignment from Imperas to Synopsys#page2.tif	

**ASSIGNMENT**

For good and valuable consideration, the receipt of which is hereby acknowledged, **Imperas Software Limited.**, a private company limited by shares organized and existing under the laws of England and Wales, with its registered office in **The Old Dairy, North Weston, Thame, Oxfordshire, OX9 2HA, United Kingdom** ("ASSIGNOR"), has sold, assigned, and transferred and does hereby sell, assign, and transfer to **Synopsys, Inc.**, a Delaware corporation, having a place of business at **675 Almanor Ave, Sunnyvale, CA 94085, U.S.A.** ("ASSIGNEE"), for itself and its successors, transferees, and assignees, the following:

1. The entire worldwide right, title, and interest in all inventions and improvements ("SUBJECT MATTER") that are disclosed in the following provisional or non-provisional application(s), including those filed under 35 U.S.C. § 111, design applications filed under 35 U.S.C. § 171, Registered Community Designs, international applications filed according to the Patent Cooperation Treaty (PCT), U.S. national phase applications filed under 35 U.S.C. § 371, national or regional applications filed in any other country throughout the world, and any patents issuing thereon ("PATENT RIGHTS"):

COUNTRY	APPLICATION NUMBER	FILING DATE	TITLE	PATENT NUMBER	ISSUE DATE
US	15/030,216	October 16, 2014	Multi-Processor Simulation on a Multi-Core Machine	11,574,087	February 7, 2023
US	12/307,636	July 2, 2009	Multiprocessor Development Environment	8,417,508	April 9, 2013
US	13/770,243	February 19, 2013	Processor Simulation Environment	9,658,849	May 23, 2017
GB	2301686.8	February 7, 2023	Verifying Central Processing Units (CPU) Using Simulation and Instruction Accurate reference Models		
GB	2306239.1	April 27, 2023	Computer-Implemented Methods of Verifying a Processor Design under Test, and Related Systems		
US	18/105,554	February 3, 2023	Multi-Processor Simulation on a Multi-Core Machine		
GB	2306912.3	May 10, 2023	A Method of Simulating a Microprocessor System		
GB	2308865.1	June 14, 2023	Application Compiler		
GB	2311038.0	July 19, 2023	A Method of Simulating an Electronic System Specified in an HDL using Just-In-Time Code Generation		

and;

2. The entire worldwide right, title, and interest in and to:  
 (a) the PATENT RIGHTS, including any right of priority; (b) any provisional, divisional, continuation, substitute, renewal, reissue, and other related applications thereto which have been or may be filed in the United States or elsewhere in the world; (c) any patents which may be granted on the applications set forth in (a) and (b) above; and (d) the right to sue in its own name and to recover for past infringement of any or all of any applications or patents issuing therefrom together with all rights to recover damages for infringement of provisional rights.

ASSIGNOR represents that ASSIGNOR has the rights, titles, and interests to convey as set forth herein, and covenants with ASSIGNEE that the ASSIGNOR has made or will make hereafter no assignment, grant, mortgage, license, or other agreement affecting the rights, titles, and interests herein conveyed.

ASSIGNOR grants the attorney of record the power to insert on this Assignment any further identification that may be necessary or desirable in order to comply with the rules of the United States Patent and Trademark Office or other authority for recordation of this document.

This Assignment may be executed in one or more counterparts, each of which shall be deemed an original and all of which may be taken together as one and the same Assignment.

Duly Authorized Representative of ASSIGNOR

Date of Signature



01 February 2024

Orla Murphy, Director  
Imperas Software Limited

Duly Authorized Representative of ASSIGNEE

Date of Signature



01 February 2024

David Pursley, VP, Legal & Deputy General Counsel  
Synopsis, Inc.