PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
Jong Min CHO	04/10/2020
Sung Bum PARK	04/10/2020
Kee Sik AHN	04/13/2020
Seong Jun PARK	04/10/2020

RECEIVING PARTY DATA

Company Name:	MagnaChip Semiconductor, Ltd.	
Street Address:	ddress: 215, Daesin-ro, Heungdeok-gu, Chungcheongbuk-do	
City:	Cheongju-si	
State/Country:	KOREA, REPUBLIC OF	
Postal Code:	28429	

PROPERTY NUMBERS Total: 2

Property Type	Number
Application Number:	17237907
Application Number:	17693887

CORRESPONDENCE DATA

Fax Number: 2023153758

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: (202)429-0020 Email: pto@nsiplaw.com

Correspondent Name: NSIP Law
Address Line 1: P.O. Box 65745

Address Line 4: Washington, DISTRICT OF COLUMBIA 20035

ATTORNEY DOCKET NUMBER:	030103.0017D1-D1C1
NAME OF SUBMITTER:	Isabelle Osborne
SIGNATURE: Isabelle Osborne	
DATE SIGNED:	03/08/2024

Total Attachments: 6

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PATENT REEL: 066704 FRAME: 0468

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PATENT REEL: 066704 FRAME: 0469

DECLARATION

OFMICONDUCTOR REVICE HAVING A RIORE TYPE ELECTRICAL EURE (E EURE) CELL			
filed on (if applicable), entitled:			
or to United States Application Number or PCT International Application Number			
As a below-named inventor, I hereby declare that this declaration is directed to the application attached hereto			

SEMICONDUCTOR DEVICE HAVING A DIODE TYPE ELECTRICAL FUSE (E-FUSE) CELL ARRAY

The above-identified application was made or authorized to be made by me.

I believe I am the original inventor or an original joint inventor of a claimed invention in the above-identified application.

I hereby acknowledge that any willful false statement made in this declaration is punishable under 18 USC 1001 by fine or imprisonment of not more than five (5) years, or both.

I have reviewed and understand the contents of the above-identified application, including the claims.

I am aware of the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in 37 CFR 1.56, including for continuation-in-part applications, material information that became available between the filing date of the prior application and the national or PCT international filing date of the continuation-in-part application.

ASSIGNMENT

For valuable consideration, I, as a below-named assignor, hereby assign to:

MagnaChip Semiconductor, Ltd. 215, Daesin-ro, Heungdeok-gu Cheongju-si, Chungcheongbuk-do, 28429 Republic of Korea

and its suc	cessors and assigr	ns (collectively herei	nafter called "the Ass	signee"), the entire right, title	, and
interest thre	oughout the world	in the inventions and	l improvements that	are the subject of the applica	ation
identified in	the above declara	ation, which is United	States Application	Number or PCT International	l Application
Number	16/848,913	_ filed on _	April 15, 2020	·	

I authorize and request the attorneys appointed in the above-identified application to hereafter complete this Assignment by inserting above the application number and the filing date of the above-identified application when known, and to correct any typographical errors that may be discovered in this Assignment.

This Assignment includes the above-identified application, any and all continuation applications and divisional applications claiming the benefit of the above-identified application under the United States patent laws, any and all United States and foreign patents, utility models, and design registrations granted for any of the inventions and improvements that are the subject of the above-identified application, and the right to claim priority based on the filing date of the above-identified application under the International Convention for the Protection of Industrial Property, the Patent Cooperation Treaty, the European Patent Convention, and all other treaties of like purposes; and I authorize the Assignee to apply in all countries in my name or in its own name for patents, utility models, design registrations, like rights of exclusion, and inventors' certificates for any of the inventions and improvements that are the subject of the above-identified application; and I agree for myself and my heirs, legal representatives, and assigns without further compensation to perform such lawful

Page 1 of 2

acts and to sign such further applications, Assignments, preliminary statements, and other lawful documents as the Assignee may reasonably request to effectuate fully this Assignment.

In witness whereof, I, as a below-named inventor and assignor, intending to be legally bound, have hereunto affixed my signature on the date indicated below next to my signature.

Inventor's Legal Name	Jong Min CHO		
Inventor's Signature	· Ari	Date	2020/4/10.
Residence (City, Country)	Cheongju-si, Republic of Korea		
Mailing Address	c/o MagnaChip Semiconductor Ltd., 215, Da Chungcheongbuk-do, 28429, Republic of Ko		ungduk-gu, Cheongju-si,
Inventor's Legal Name	Sung Bum PARK		
Inventor's Signature	12/h	Date	Apr. 10, 2020.
Residence (City, Country)	Seongnam-si, Republic of Korea		
Mailing Address	c/o MagnaChip Semiconductor Ltd., 215, Da Chungcheongbuk-do, 28429, Republic of Ko		eungduk-gu, Cheongju-si,
Inventor's Legal Name	Kee Sik AHN		
Inventor's Signature		Date	
Residence (City, Country)	Hwaseong-si, Republic of Korea		
Mailing Address	c/o MagnaChip Semiconductor Ltd., 215, Da Chungcheongbuk-do, 28429, Republic of Ko		eungduk-gu, Cheongju-si,
Inventor's Legal Name	Seong Jun PARK		
Inventor's Signature		Date	
Residence (City, Country)	Suwon-si, Republic of Korea		
Mailing Address	c/o MagnaChip Semiconductor Ltd., 215, Daesin-ro, Heungduk-gu, Cheongju-si, Chungcheongbuk-do, 28429, Republic of Korea		

DECLARATION
As a below-named inventor, I hereby declare that this declaration is directed to the application attached hereto,
or to United States Application Number or PCT International Application Number
filed on (if applicable), entitled:
SEMICONDUCTOR DEVICE HAVING A DIODE TYPE ELECTRICAL FUSE (E-FUSE) CELL ARRAY
The above-identified application was made or authorized to be made by me.
I believe I am the original inventor or an original joint inventor of a claimed invention in the above-identified application.
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I am aware of the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in 37 CFR 1.56, including for continuation-in-part applications, material information that became available between the filing date of the prior application and the national or PCT international filing date of the continuation-in-part application.
ASSIGNMENT
For valuable consideration, i, as a below-named assignor, hereby assign to:
MagnaChip Semiconductor, Ltd. 215, Daesin-ro, Heungdeok-gu Cheongju-si, Chungcheongbuk-do, 28429 Republic of Korea
and its successors and assigns (collectively hereinafter called "the Assignee"), the entire right, title, and
interest throughout the world in the inventions and improvements that are the subject of the application
identified in the above declaration, which is United States Application Number or PCT International Application Number 16/848,913 filed on April 15, 2020
I authorize and request the attorneys appointed in the above-identified application to hereafter complete this Assignment by inserting above the application number and the filing date of the above-identified application when known, and to correct any typographical errors that may be discovered in this Assignment.
This Assignment includes the above-identified application, any and all continuation applications and divisional applications claiming the benefit of the above-identified application under the United States patent laws, any and all United States and foreign patents, utility models, and design registrations granted for any of the inventions and improvements that are the subject of the above-identified application, and the right to claim priority based on the filing date of the above-identified application under the International Convention for the

Page 1 of 2

Protection of Industrial Property, the Patent Cooperation Treaty, the European Patent Convention, and all other treaties of like purposes; and I authorize the Assignee to apply in all countries in my name or in its own name for patents, utility models, design registrations, like rights of exclusion, and inventors' certificates for any of the inventions and improvements that are the subject of the above-identified application, and I agree for myself and my heirs, legal representatives, and assigns without further compensation to perform such lawful

acts and to sign such further applications, Assignments, preliminary statements, and other lawful documents as the Assignee may reasonably request to effectuate fully this Assignment.

In witness whereof, I, as a below-named inventor and assignor, intending to be legally bound, have hereunto affixed my signature on the date indicated below next to my signature.

Inventor's Legal Name	Jong Min CHO		
Inventor's Signature		Date	
Residence (City, Country)	Cheongju-si, Republic of Korea		
Mailing Address	c/o MagnaChip Semiconductor Ltd., 215, Daesi Chungcheongbuk-do, 28429, Republic of Korea		ungduk-gu, Cheongju-si,
_			
Inventor's Legal Name	Sung Bum PARK		
Inventor's Signature		Date	
Residence (City, Country)	Seongnam-si, Republic of Korea	***************************************	
Mailing Address	c/o MagnaChip Semiconductor Ltd., 215, Daesin-ro, Heungduk-gu, Cheongju-si, Chungcheongbuk-do, 28429, Republic of Korea		
	T		
Inventor's Legal Name	Kee Sik AHN	····	
Inventor's Signature	악기식	Date	Apr. 13, 2020
Residence (City, Country)	Hwaseong-si, Republic of Korea		
Mailing Address	c/o MagnaChip Semiconductor Ltd., 215, Daesin-ro, Heungduk-gu, Cheongju-si, Chungcheongbuk-do, 28429, Republic of Korea		
Inventor's Legal Name	Seong Jun PARK		
Inventor's Signature		Date	
Residence (City, Country)	Suwon-si, Republic of Korea		
	c/o MagnaChip Semiconductor Ltd., 215, Daesin-ro, Heungduk-gu, Cheongju-si, Chungcheongbuk-do, 28429, Republic of Korea		

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	filed on	_(if applicable), entitled:
or to United States Application	Number or PCT International Application	n Number
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MagnaChip Semiconductor, Ltd. 215, Daesin-ro, Heungdeok-gu Cheongju-si, Chungcheongbuk-do, 28429 Republic of Korea

and its su	ccessors and assigns (colle	ctively hereir	nafter called "the Assignee"), the entire right, title, and
interest th	roughout the world in the in	ventions and	improvements that are the subject of the application
identified	in the above declaration, wh		States Application Number or PCT International Application
Number _	16/848,913	filed on	April 15, 2020

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Inventor's Legal Name	Jong Min CHO		
Inventor's Signature		Date	
Residence (City, Country)	Cheongju-si, Republic of Korea		
Mailing Address	c/o MagnaChip Semiconductor Ltd., 215, Daesin-ro, Heungduk-gu, Cheongju-si, Chungcheongbuk-do, 28429, Republic of Korea		
		.	
Inventor's Legal Name	Sung Bum PARK		
Inventor's Signature		Date	
Residence (City, Country)	Seongnam-si, Republic of Korea		
Mailing Address	c/o MagnaChip Semiconductor Ltd., 215, Daesin-ro, Heungduk-gu, Cheongju-si, Chungcheongbuk-do, 28429, Republic of Korea		
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Inventor's Signature		Date	
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Mailing Address	c/o MagnaChip Semiconductor Ltd., 215, Daesin-ro, Heungduk-gu, Cheongju-si,		

Inventor's Legal Name	Seong Jun PARK		
Inventor's Signature	外传文	Date	Apr. 10, 2020
Residence (City, Country)	Suwon-si, Republic of Korea		
Mailing Address	c/o MagnaChip Semiconductor Ltd., 2		ungduk-gu, Cheongju-si,

Chungcheongbuk-do, 28429, Republic of Korea

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Mailing Address