

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

Assignment ID: PATI89426

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
pSemi Corporation	02/24/2024
RECEIVING PARTY DATA	
Company Name:	Murata Manufacturing Co., Ltd.
Street Address:	10-1, Higashikotari 1-chome
City:	Nagaokakyo-shi
State/Country:	JAPAN
Postal Code:	617-8555
PROPERTY NUMBERS Total: 12	
Property Type	Number
Patent Number:	10484038
Patent Number:	10715199
Patent Number:	10965026
Patent Number:	11005460
Patent Number:	11057023
Patent Number:	11057030
Patent Number:	11057031
Patent Number:	11290105
Patent Number:	11777498
Patent Number:	11277109
Patent Number:	11601098
Patent Number:	11742848
CORRESPONDENCE DATA	
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<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
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NAME OF SUBMITTER:	Stephanie Nunez Dominguez
SIGNATURE:	Stephanie Nunez Dominguez
DATE SIGNED:	03/13/2024
Total Attachments: 5 source=US_Assignment_pSemi_MMC_signed#page1.tif source=US_Assignment_pSemi_MMC_signed#page2.tif source=US_Assignment_pSemi_MMC_signed#page3.tif source=US_Assignment_pSemi_MMC_signed#page4.tif source=US_Assignment_pSemi_MMC_signed#page5.tif	

ASSIGNMENT

pSemi Corporation, an entity having a place of business at 9369 Carroll Park Drive, San Diego, CA 92121, U.S.A., hereinafter also referred to as the assignor,

hereby sells and assigns for valuable consideration

to Murata Manufacturing Co., Ltd., an entity having a place of business at 10-1, Higashikotari 1-chome, Nagaokakyo-shi, Kyoto 617-8555, Japan, hereinafter also referred to as the assignee,

its entire right, title and interest, including all priority rights under U.S. law and/or International conventions and treaties, in and to the US, International and/or foreign patents and applications listed in the attached Annex A.

Authorized to act for and on behalf of the assignor, pSemi Corporation

Assignor's Officer Name: Ibrahim Sezan

Assignor's Office Title: Director, Technology & IP Strategy

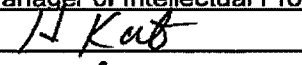
Assignor's Officer Signature: 

Date: 2/24/2024

Authorized to act for and on behalf of the assignee, Murata Manufacturing Co., Ltd.

Assignee's Officer Name: Hitoshi Katakura

Assignee's Office Title: General Manager of Intellectual Property Department

Assignee's Officer Signature: 

Date: 6 Mar., 2024

Annex A

	S+B Ref.	Assignor Ref.	App No.	Filing Date	Issue No.	Issue Date	Status	Title
1	P2346-US	PER-297-PAP	16/455,427	2019-06-27	10484038	2019-11-19	Granted	PHASED ARRAY TRANSCEIVER WITH BUILT-IN PHASE INTERFEROMETER
2	P2346-USC	PER-297-CON-1	16/601,302	2019-10-14	10715199	2020-07-14	Granted	PHASED ARRAY TRANSCEIVER WITH BUILT-IN PHASE INTERFEROMETER
3	P2346-US2	PER-297-PCT-US	17/558,106	2021-12-21			Pending	PHASED ARRAY TRANSCEIVER WITH BUILT-IN PHASE INTERFEROMETER AND/OR TRANSMITTER LINEARIZATION FEEDBACK
4	P2347-US	PER-298-PAP	16/455,517	2019-16-27	10965026	2021-03-30	Granted	PHASED ARRAY TRANSCEIVER WITH BUILT-IN TRANSMITTER LINEARIZATION FEEDBACK
5	P2447-US	PER-325-PAP	16/934,866	2020-06-21	11005460	2021-05-11	Granted	FLYING LEVEL SHIFTER FOR NARROW PULSES
6	P2448-US	PER-326-PAP	16/839,718	2020-04-03	11057023	2021-07-06	Granted	ACCURATE HIGH-VOLTAGE GATE DETECTOR
7	P2449-US	PER-327-PAP	16/992,001	2020-08-12	11057030	2021-07-06	Granted	RELIABILITY IN START UP SEQUENCE FOR D-MODE POWER FET DRIVER
8	P2465-US	PER-334-PAP	16/992,003	2020-08-12	11057031	2021-07-06	Granted	RELIABILITY IN START UP SEQUENCE FOR D-MODE POWER FET DRIVER

9	P2472-US	PER-335-PAP	17/129,568	2020-12-21	11290105	2022-03-29	Granted	HIGH POWER RF SWITCH WITH CONTROLLED WELL VOLTAGE FOR IMPROVED LINEARITY
10	P2472-USC	PER-335-CON-1	17/698,998	2022-03-18	11777498	2023-10-03	Granted	HIGH POWER RF SWITCH WITH CONTROLLED WELL VOLTAGE FOR IMPROVED LINEARITY
11	P2529-USBC	PER-350-PCT-US	18/170,447	2023-02-16			Pending	METHODS AND SYSTEMS FOR DYNAMIC RF BAND ALLOCATION
12	P2544-US	PER-360-PAP	17/171,860	2021-02-09	11277109	2022-03-15	Granted	DISABLED INPUT FOR SWITCH LNA INPUT ATTENUATION
13	P2553-US	PER-364-PAP	17/214,712	2021-03-26	11601098	2023-03-07	Granted	DIFFERENTIAL CASCODE AMPLIFIER ARRANGEMENT WITH REDUCED COMMON MODE GATE RF VOLTAGE
14	P2553-USBC	PER-364-PCT-US	18/465,825	2023-09-12			Pending	DIFFERENTIAL CASCODE AMPLIFIER ARRANGEMENT WITH REDUCED COMMON MODE GATE RF VOLTAGE
15	P2574-US	PER-376-PAP	17/351,128	2021-06-17			Pending	SWITCH CAPACITANCE CANCELLATION CIRCUIT
16	P2594-US	PER-383-PAP	17/351,131	2021-06-17			Pending	STACKED MULTI-STAGE PROGRAMMABLE LNA ARCHITECTURE
17	P2608-US	PER-388-PAP	17/523,584	2021-11-10			Pending	HYBRID INPUT LNA RF FRONTEND ARCHITECTURE

18	P2628-US	PER-397-PAP	17/830,639	2022-06-02			Pending	CIRCUITS AND METHODS FOR GENERATING A CONTINUOUS CURRENT SENSE SIGNAL
19	P2675-US	PER-412-PAP	17/673,372	2022-02-16	11742848	2023-08-29	Granted	HIGH-VOLTAGE TRANSMISSION GATE ARCHITECTURE
20	P2668-US	PER-416-PAP	18/153,039	2023-01-11			Pending	METHODS FOR SIMULTANEOUS GENERATION OF A TRAP-RICH LAYER AND A BOX LAYER
21	P2747-US	PER-447-PAP	17/934,787	2022-09-23			Pending	DRIVER WITH BUILT-IN TESTING OF SWITCH STATUS
22	P2747-USCIP	PER-447-CIP	18/350,325	2023-07-11			Pending	TEMPERATURE-CONTROLLED DRIVER WITH BUILT-IN TESTING OF SWITCH STATUS
23	P2748-US	PER-448-PAP	17/815,193	2022-07-26			Pending	INTEGRATED PCM DRIVER
24	P2771-US	PER-457-PAP	18/147,607	2022-12-28			Pending	LNA ARCHITECTURE WITH CONFIGURABLE FEEDBACK FILTERING
25	P2828-US	PER-473-PAP	18/457,225	2023-08-28			Pending	SELECTIVELY COUPLED POWER SPLITTER
26	P2829-US	PER-474-PAP	18/335,549	2023-06-15			Pending	WIDEBAND LNA WITH OUTPUT MATCH CONFIGURABILITY
27	P2929-USP	PER-495-PROV	63/502,202	2023-05-15			Pending	WIDEBAND NOISE SHAPING SPLIT LNA ARCHITECTURE
28	P2929-US	PER-495-PAP	18/449,221	2023-08-14			Pending	WIDEBAND NOISE SHAPING SPLIT LNA ARCHITECTURE

29	P2930-USP	PER-496-PROV	63/502,263	2023-05-15		Pending	WIDEBAND SWITCHING GAIN ENHANCED TUNABLE LNA ARCHITECTURE
30	P2930-US	PER-496-PAP	18/452,467	2023-08-18		Pending	WIDEBAND SWITCHING GAIN ENHANCED TUNABLE LNA ARCHITECTURE