PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT	
NATURE OF CONVEYANCE:	ASSIGNMENT	
SEQUENCE:	1	

CONVEYING PARTY DATA

Name	Execution Date
Hui Zhao	09/15/2022
Fei Lu	09/15/2022
Yuxiang Sun	09/15/2022
Zhendong Guo	09/15/2022

RECEIVING PARTY DATA

Company Name:	Marvell Semiconductor, Inc.	
Street Address:	5488 marvell lane	
City:	Santa Clara	
State/Country:	CALIFORNIA	
Postal Code:	95054	

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	17932707

CORRESPONDENCE DATA

Fax Number:

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 4082222500

Email: syzemore@marvell.com

Correspondent Name: Kelvin Vivian
Address Line 1: 5488 marvell lane

Address Line 4: santa clara, CALIFORNIA 95054

NAME OF SUBMITTER:	Chris Syzemore	
SIGNATURE:	Chris Syzemore	
DATE SIGNED:	06/18/2024	

Total Attachments: 2

source=MP13448 EXECUTED Assignment-Inventors to MSI#page1.tif source=MP13448 EXECUTED Assignment-Inventors to MSI#page2.tif

PATENT 508607335 REEL: 067760 FRAME: 0929

Docket#: MP13448/004048-0851-101 PATENT

ASSIGNMENT

For good and valuable consideration, the receipt of which is hereby acknowledged, each of us, the undersigned inventors,

Hui Zhao, Fei Lu, Yuxiang Sun and Zhendong Guo

who has created a certain invention for which one or more patent applications identified below (hereinafter called "the Patent Application(s)") that have been, or are to be, filed:

Marvell Reference No.	Application No.	Filing Date	<u>Title</u>
MP13448	Not Yet Assigned	Not Yet Assigned	OSCILLATOR FOR HIGH-SPEED SERIAL DEVICE
MP13448PR	63/246,709	September 21, 2021	LC DIFFERENTIAL OSCILLATOR STRUCTURE WITH BETTER QUALITY FACTOR AND PULLING MITIGATION

Does hereby sell, assign and transfer to Marvell Semiconductor, Inc., a corporation of California, having a place of business at 5488 Marvell Lane, Santa Clara, California 95054, its successors, assigns, and legal representatives, the full and exclusive right to said invention and said Patent Application(s) and to any and all inventions described in said Patent Application(s), including applications for Letters Patent for the United States, its territorial possessions, and all foreign countries, and the entire right, title and interest in and to any and all Letters Patent which may be granted therefor in the United States, its territorial possessions, and all foreign countries; to any and all Letters Patent which may be entitled to the benefit thereof in the United States, its territorial possessions, and all foreign countries; and in and to any and all continuations-in-part, continuations, divisions, substitutes, reissues, extensions thereof, and all other applications for Letters Patent relating thereto which have been or shall be filed in the United States, its territorial possessions, and/or any foreign countries, and all rights, together with all priority rights, under any of the international conventions, unions, agreements, acts, and treaties;

Authorizes and requests that the attorneys of record in any of said Patent Application(s) insert in this assignment the filing date(s) and application number(s) of said Patent Application(s) when officially known;

Agrees that Marvell Semiconductor, Inc., hereinafter referred to as Assignee, may apply for and receive Letters Patent for said invention and said inventions, hereinafter referred to as said invention, in its own name, in the United States, its territorial possessions, and all foreign countries; and that, when requested to carry out in good faith the intent and purpose of this assignment, at the expense of said Assignee, its successors, assigns and legal representatives, the undersigned inventors will execute all continuations-in-part, continuations, divisions, substitutes, reissues, extensions thereof, execute all rightful oaths, assignments, powers of attorney and other papers, testify in any legal or quasi legal proceedings; communicate to said Assignee, its successors, assigns or legal representatives all facts known to the undersigned inventors relating to said invention and the history thereof; and generally do everything possible which said Assignee, its successors, assigns, or legal representatives shall consider desirable for aiding in securing, maintaining and enforcing proper patent protection for said invention and for vesting title to said invention and all applications for patents on said invention in said Assignee, its successors, assigns, or legal representatives; and

Covenants with said Assignee, its successors, assigns, or legal representatives that no assignment, grant, mortgage, license or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned inventors, and that full right to convey the same as herein expressed is possessed by the undersigned inventor.

IN TESTIMONY WHEREOF we have hereunto set our signatures on the dates indicated below.

PATENT REEL: 067760 FRAME: 0930

Date:

RECORDED: 06/18/2024

Docket#: MP13448/004048-0851-101 PATENT

Full Name of First Inventor:	
Hui Zhao	
Inventor's Signature:	Date: Month/Day/Year
Hom	September 15, 2022
Full Name of Second Inventor:	
Fei Lu	
Inventor's Signature:	Date: Month/Day/Year
Fiilu	September 15, 2022
1 Q (M	
Full Name of Third Inventor:	
Yuxiang Sun	
Inventor's Signature:	Date: Month/Day/Year
Una iona Cun	September 15, 2022
Yuxiang Sun	
Full Name of Fourth Inventor:	
Zhendong Guo	
Inventor's Signature:	Date: Month/Day/Year
Eliendong Guo	September 15, 2022
undersigned representative Authorizes and requests t	ed to sign this certificate on behalf of Assignee. Assigne that the attorneys of record in any of said Patent Application cation number(s) of said Patent Application(s) when officia
Accepted on behalf of:	
Marvell Semiconductor, Inc.	
By: <u>Luin Vivian</u>	
Name: Kelvin Vivian	
Title: Senior Director, Associate General Counsel, September 15, 2022	Intellectual Property

PATENT PATENE: 0931