

<b>PATENT ASSIGNMENT COVER SHEET</b>
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Assignment ID: PATI468122

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
Teknologian tutkimuskeskus VTT Oy	08/09/2024
<b>RECEIVING PARTY DATA</b>	
<b>Company Name:</b>	Flow-computing Oy
<b>Street Address:</b>	c/o Maria 01
<b>Internal Address:</b>	Lapinlahdenkatu 16
<b>City:</b>	Helsinki
<b>State/Country:</b>	FINLAND
<b>Postal Code:</b>	00180
<b>PROPERTY NUMBERS Total: 6</b>	
<b>Property Type</b>	<b>Number</b>
<b>Application Number:</b>	17415890
<b>Application Number:</b>	18470480
<b>Application Number:</b>	14892446
<b>Application Number:</b>	15031285
<b>Application Number:</b>	15105583
<b>Application Number:</b>	16066355
<b>CORRESPONDENCE DATA</b>	
<b>Fax Number:</b>	
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
<b>Phone:</b>	2036590388
<b>Email:</b>	ptomail@gziplaw.com
<b>Correspondent Name:</b>	Mr. Geza Ziegler Jr.
<b>Address Line 1:</b>	2000 Post Road
<b>Address Line 2:</b>	Suite 205
<b>Address Line 4:</b>	Fairfield, CONNECTICUT 06824
<b>ATTORNEY DOCKET NUMBER:</b>	MD7831
<b>NAME OF SUBMITTER:</b>	Geza Ziegler
<b>SIGNATURE:</b>	Geza Ziegler
<b>DATE SIGNED:</b>	09/02/2024

**PATENT**

**Total Attachments: 3**

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Assignment of Rights, Title and Interest in Invention				Docket No.		
<i>This is an Assignment of the following rights, title and interest: (check all that apply):</i>						
Title	AttorneyRef	ApplicationNum	IssueDate	FilingDate	PatentNum	ReelFrame
METHOD AND ARRANGEMENT FOR HANDLING MEMORY ACCESS FOR A TCF-AWARE PROCESSOR	MD.7831.US N1	17/415,890	8/6/2024	6/18/2021	12056495	056852/0207
FLOATING-POINT SUPPORTIVE PIPELINE FOR EMULATED SHARED MEMORY ARCHITECTURES	MD.7832.US N1	18/470,480		9/20/2023		039217/0152
MEMORY UNIT FOR DATA MEMORY REFERENCES OF MULTI-THREADED PROCESSOR WITH INTERLEAVED INTER-THREAD PIPELINE IN EMULATED SHARED MEMORY ARCHITECTURES	MD.8039	14/892,446	9/11/2018	11/19/2015	10073782	037942/0097
FLOATING-POINT SUPPORTIVE PIPELINE FOR EMULATED SHARED MEMORY ARCHITECTURES	MD.8039	15/031,285	10/24/2023	4/22/2016	11797310	039217/0152
ARCHITECTURE FOR LONG LATENCY OPERATIONS IN EMULATED SHARED MEMORY ARCHITECTURES	MD.8039	15/105,583	11/13/2018	6/17/2016	10127048	039591/0215
MEMORY NODE WITH CACHE FOR EMULATED SHARED MEMORY COMPUTERS	MD.8039	16/066,355	7/13/2021	6/27/2018	11061817	046222/0975
<b><u>Assignor (Corporation)</u></b>						
<b><i>Name</i></b>				<b><i>Address</i></b>		
Teknologian tutkimuskeskus VTT Oy				Tekniikantie 21 02150 Espoo, Finland		

<i><u>Assignee (Corporation)</u></i>	
<i>Name</i>	<i>Address</i>
Flow-computing Oy	c/o Maria 01 Lapinlahdenkatu 16 00180 HELSINKI, Finland

<b>Assignment of Rights, Title and Interest in Invention</b>	<b>Docket No.</b> MD.7831.USN1
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Whereas, the above-identified Assignor states that it is the current assignee of the entire right, title, and interest in the above-identified patent application(s) and/or patent(s) (hereinafter referred to as "Invention") by virtue of an assignment from the inventor(s) of the above-identified Invention. The assignment was recorded in the United States Patent and Trademark Office at **Reel / Frame: {Matter.RealFrame}**, or for which a copy therefore is attached;

And, whereas the Assignor desires to assign its above-identified rights, title and interest in the Invention to the above-identified Assignee;

Now, this indenture witnesseth, that for good and valuable consideration, the receipt whereof is hereby acknowledged;

The Assignor does hereby assign, sell and transfer its above-identified rights, title and interest in said Invention, said application(s) as identified above, including any divisions, continuations, and continuations-in-part thereof, and in and to any and all Letters Patent of the United States, and countries foreign thereto, which may be granted or have granted for said Invention, and in and to any and all reissues and reexaminations thereof, and in and to any and all priority rights, Convention rights, and other benefits accruing or to accrue to us with respect to the filing of applications for patents or securing of patents in the United States and countries foreign thereto, unto said Assignee;

And the Assignor does hereby authorize and request the Director of the United States Patent and Trademark Office to issue any United States Letters Patent which may issue for said Invention to said Assignee, as assignee of the whole right, title and interest thereto;

And the Assignor does further agree to sign and execute all necessary and lawful future documents, including applications for foreign patents, for filing divisions, continuations and continuations-in-part of said application for patent, and/or for obtaining any reissue or reissues of any Letters Patent which may be granted for my aforesaid Invention, as the Assignee or its Designee(s) may from time to time require and prepare at its own expense.

*Assignor Signature*

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignor.

Teknologian tutkimuskeskus VTT Oy  
**Assignor**

Signed by:  
Jarmo Rantala  
079B66791EAD49C...  
**By**

09.08.2024  
**Date**

Jarmo Rantala  
**Printed or Typed Name**

Patent Attorney  
**Title**