

## PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1  
Stylesheet Version v1.2

Assignment ID: PATI556039

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
S. Aqua Semiconductor, LLC	10/10/2024
<b>RECEIVING PARTY DATA</b>	
<b>Company Name:</b>	Intellectual Ventures Assets 199 LLC
<b>Street Address:</b>	251 Little Falls Drive
<b>City:</b>	Wilmington
<b>State/Country:</b>	DELAWARE
<b>Postal Code:</b>	19808
<b>PROPERTY NUMBERS Total: 3</b>	
<b>Property Type</b>	<b>Number</b>
<b>Application Number:</b>	10850719
<b>Application Number:</b>	60475224
<b>Application Number:</b>	11771689
<b>CORRESPONDENCE DATA</b>	
<b>Fax Number:</b>	
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
<b>Phone:</b>	(425)283-4893
<b>Email:</b>	ivrecording@intven.com
<b>Correspondent Name:</b>	Intellectual Ventures Management - IP Legal
<b>Address Line 1:</b>	3150 - 139th Ave SE Building 4
<b>Address Line 2:</b>	5th Floor
<b>Address Line 4:</b>	Bellevue, WASHINGTON 98005
<b>NAME OF SUBMITTER:</b>	Joann Reynolds
<b>SIGNATURE:</b>	Joann Reynolds
<b>DATE SIGNED:</b>	10/10/2024
<b>Total Attachments: 3</b>	
source=4 S. Aqua Semiconductor, LLC Assignment of Patent Rights - to Intellectual Ventures Assets 199 LLC_signed#page1.tiff	
source=4 S. Aqua Semiconductor, LLC Assignment of Patent Rights - to Intellectual Ventures Assets 199 LLC_signed#page2.tiff	



## ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, S. Aqua Semiconductor, LLC, a Delaware limited liability company having an address at 251 Little Falls Drive, Wilmington, DE 19808 (“**Assignor**”), does hereby sell, assign, transfer, and convey unto Intellectual Ventures Assets 199 LLC, a Delaware limited liability company having an address at 251 Little Falls Drive, Wilmington, DE 19808 (“**Assignee**”), or its designees, all of its right, title, and interest that exist today and may exist in the future in and to any and all of the following (collectively, the “**Patent Rights**”):

(a) the provisional patent applications, patent applications and patents listed in the table below (the “**Patents**”);

### Active Patent(s) – (Filed; Granted)

<u>Patent/Application Number</u>	<u>Country</u>	<u>Issue Date/ Filing Date</u>	<u>Title of Patent and First Named Inventor</u>
7254690 (10/850719)	US	Aug 7 2007 (May 20 2004)	PIPELINED SEMICONDUCTOR MEMORIES AND SYSTEMS  G.R. Mohan Rao

(b) all patents and patent applications (i) to which any of the Patents directly or indirectly claims priority, and/or (ii) for which any of the Patents directly or indirectly forms a basis for priority;

(c) all reissues, reexaminations, extensions, continuations, continuations in part, continuing prosecution applications, requests for continuing examinations, divisions, registrations of any item in any of the foregoing categories (a) and (b);

(d) all foreign patents, patent applications, and counterparts relating to any item in any of the foregoing categories (a) through (c), including, without limitation, certificates of invention, utility models, industrial design protection, design patent protection, and other governmental grants or issuances;

(e) all items in any of the foregoing in categories (b) through (d), whether or not expressly listed as Patents above and whether or not claims in any of the foregoing have been rejected, withdrawn, cancelled, or the like;

(f) inventions, invention disclosures, and discoveries described in any of the Patents and/or any item in the foregoing categories (b) through (e) that (i) are included in any claim in the Patents and/or any item in the foregoing categories (b) through (e), (ii) are subject matter capable of being reduced to a patent claim in a reissue or reexamination proceeding brought on any of the Patents and/or any item in the foregoing categories (b) through (e), and/or (iii) could have been included as a claim in any of the Patents and/or any item in the foregoing categories (b) through (e);

(g) all rights to apply in any or all countries of the world for patents, certificates of invention, utility models, industrial design protections, design patent protections, or other

governmental grants or issuances of any type related to any item in any of the foregoing categories (a) through (f), including, without limitation, under the Paris Convention for the Protection of Industrial Property, the International Patent Cooperation Treaty, or any other convention, treaty, agreement, or understanding;

(h) all causes of action (whether known or unknown or whether currently pending, filed, or otherwise) and other enforcement rights under, or on account of, any of the Patents and/or any item in any of the foregoing categories (b) through (g), including, without limitation, all causes of action and other enforcement rights for

- (1) damages,
- (2) injunctive relief, and
- (3) any other remedies of any kind

for past, current, and future infringement;

(i) all rights to collect royalties and other payments under or on account of any of the Patents and/or any item in any of the foregoing categories (b) through (h); and

(j) all rights to the assets listed in the table below, as well as all rights to the inventions, invention disclosures, and discoveries in the assets listed in the table below, together, with the rights, if any, to revive prosecution of claims under such assets and to sue or otherwise enforce any claims under such assets for past, present or future infringement:

**Inactive Patent(s) – (Abandoned; Lapsed; Expired)**

<b><u>Patent/Application Number</u></b>	<b><u>Country</u></b>	<b><u>Issue Date/ Filing Date</u></b>	<b><u>Title of Patent and First Named Inventor</u></b>
(60/475224)	US	(Jun 2 2003)	Pipelined semiconductor memories G.R. Mohan Rao
(11/771689)	US	(Jun 29 2007)	PIPELINED SEMICONDUCTOR MEMORIES AND SYSTEMS G. R. Mohan Rao

Assignor hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents, certificates of invention, utility models or other governmental grants or issuances that may be granted upon any of the Patent Rights in the name of Assignee, as the assignee to the entire interest therein.

Assignor will, at the reasonable request of Assignee, do all things necessary, proper, or advisable, including without limitation, the execution, acknowledgment, and recordation of specific assignments, oaths, declarations, and other documents on a country-by-country basis, to assist Assignee in obtaining, perfecting, sustaining, and/or enforcing the Patent Rights.

The terms and conditions of this Assignment of Patent Rights will inure to the benefit of Assignee, its successors, assigns, and other legal representatives and will be binding upon Assignor, its successors, assigns, and other legal representatives.

IN WITNESS WHEREOF this Assignment of Patent Rights is executed at Bellevue, WA on October 10, 2024.

**ASSIGNOR:**

**S. Aqua Semiconductor, LLC**

By: *M Macartney*

Name: Michelle Macartney

Title: Authorized Person

**ASSIGNEE:**

**Intellectual Ventures Assets 199 LLC**

By: *M Macartney*

Name: Michelle Macartney

Title: Authorized Person