

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
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Assignment ID: PATI656252

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
SEQUENCE:	2
CONVEYING PARTY DATA	
Name	Execution Date
Microsoft Corporation	10/10/2024
RECEIVING PARTY DATA	
Company Name:	Microsoft Technology Licensing, LLC
Street Address:	One Microsoft Way
City:	Redmond
State/Country:	WASHINGTON
Postal Code:	98052
PROPERTY NUMBERS Total: 19	
Property Type	Number
Application Number:	61232353
Application Number:	61877204
Application Number:	16224639
Application Number:	15177082
Application Number:	14045173
Application Number:	14045187
Patent Number:	8022526
Patent Number:	8381031
Patent Number:	8554506
Patent Number:	8555096
Patent Number:	8675371
Patent Number:	9220176
Patent Number:	9429983
Patent Number:	9645603
Patent Number:	9778730
Patent Number:	10162379
Patent Number:	10437316
Patent Number:	11042211
PCT Number:	US1044800

CORRESPONDENCE DATA**Fax Number:**

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 5035957300**Email:** cpolance@allemanhall.com**Correspondent Name:** Catherine Polance**Address Line 1:** 900 SW 5th Ave., Suite 2300**Address Line 4:** Portland, OREGON 97204

NAME OF SUBMITTER:	Mrs. Catherine Polance
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SIGNATURE:	Mrs. Catherine Polance
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DATE SIGNED:	11/25/2024
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Total Attachments: 4

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source=2024-10-09 Executed Assignment (Microsoft Corporation to MTL)#page2.tiff

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ASSIGNMENT OF PATENTS AND APPLICATIONS

Now, therefore, for good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged,

Microsoft Corporation, a corporation organized under the laws of Washington and having an office and place of business at One Microsoft Way, Redmond, Washington 98052 ("ASSIGNOR"),

hereby sells, assigns, transfers, and conveys to

Microsoft Technology Licensing, LLC, a limited liability company organized under the laws of Washington and having an office and place of business at One Microsoft Way, Redmond, Washington 98052 ("ASSIGNEE"),

all right, title and interest ASSIGNOR has in and to the patents and patent applications listed in Exhibit A, ("PATENTS") attached hereto, including any and all legal rights to sue for past, present and future infringement, to collect royalties, to prosecute all existing patent applications worldwide, to apply for additional patents worldwide and to have patents issue in the name of ASSIGNEE.

Assignor also assigns and transfers to the Assignee and the Assignee's legal representatives, successors and assigns, its full and exclusive rights in and to the Intellectual Property including the PATENTS in the U.S. and every foreign country, and its entire right, title, and interest in and to the related applications (e.g., provisional applications, non-provisional applications, continuations, continuations-in-part, divisionals, reissues, reexaminations, National phase applications, including petty patent applications, and utility model applications) that may be filed in the United States and every foreign country on the Intellectual Property including the PATENTS and related applications.

Upon said consideration, Assignor conveys to the Assignee the right to make application in its own behalf on the Intellectual Property including the PATENTS and related applications in the U.S. and countries foreign to the U.S. and to claim under the Patent Cooperation Treaty, the International Convention and/or other international arrangement for any such application the date of any earlier U.S. application (or any other application on the invention) to gain priority with respect to other applications.


(1) ASSIGNOR requests and authorizes the appropriate officials in any country, to issue any and all Letters Patent that may be granted upon any patent application or any additional, continuing or divisional applications thereof to the ASSIGNEE, its successors and assigns.

(2) ASSIGNOR grants to ASSIGNEE and its agents the authority and power to insert on this instrument any further identification necessary or desirable for purposes of recordation in the Patent and Trademark Office of any country.

(3) ASSIGNOR certifies that the person signing this assignment is a lawful representative of ASSIGNOR and is authorized to execute this assignment its behalf.


(4) This assignment will become effective on the date when both ASSIGNOR and ASSIGNEE have signed it.

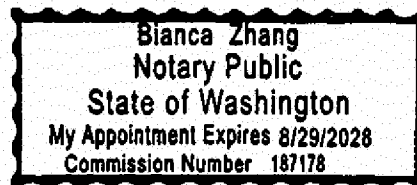
10/10/24
[Date]

Signature of  for ASSIGNOR
Dan Choi, Assistant Secretary, Microsoft Corporation

I certify that the above-identified ASSIGNOR, personally known to me to be the same person or persons who signed the foregoing instrument and acknowledged that the said instrument was signed and delivered as the free and voluntary act of the ASSIGNOR, for the uses and purposes therein set forth.

Given under my hand and seal, this October 10, 2024 [Date].





10/24/24
[Date]

Signature of Burton Davis for ASSIGNEE.
Burton Davis, President, Microsoft Technology Licensing, LLC

I certify that the above-identified ASSIGNEE, personally known to me to be the same person or persons who signed the foregoing instrument and acknowledged that the said instrument was signed and delivered as the free and voluntary act of the ASSIGNEE, for the uses and purposes therein set forth.

Given under my hand and seal, this October 24, 2024 [Date].

Blanca Zhang

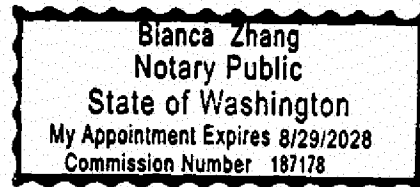


EXHIBIT A

Patent Application Number	Country	Filing Date	Title of Application
61/232,353	US	8/7/2009	SYSTEM AND METHOD FOR DISTRIBUTION COMPUTING
PCT/US2010/044800	WO	8/6/2010	DISTRIBUTED COMPUTING
12/852,409	US	8/6/2010	Distributed computing
12/852,381	US	8/6/2010	Distributed computing
12/852,430	US	8/6/2010	Distributed computing
			Method and apparatus for selectively placing components into a sleep mode in response to loss of one or more clock signals or receiving a command to enter sleep mode
12/852,406	US	8/6/2010	
12/852,423	US	8/6/2010	Distributed computing
13/769,071	US	2/16/2013	Integrated circuit arrangement in a distributed computing system
			SYSTEM CLOCK DISTRIBUTION IN A DISTRIBUTED COMPUTING ENVIRONMENT
61/877,204	US	9/12/2013	
14/045,173	US	10/3/2013	DISTRIBUTED COMPUTING
14/045,187	US	10/3/2013	DISTRIBUTED COMPUTING
			System clock distribution in a distributed computing environment
14/483,528	US	9/11/2014	
			System clock distribution in a distributed computing environment
14/823,232	US	8/11/2015	
14/942,594	US	11/16/2015	Sleep mode initialization in a distributed computing system
			SYSTEM CLOCK DISTRIBUTION IN A DISTRIBUTED COMPUTING ENVIRONMENT
15/177,082	US	6/8/2016	
			System clock distribution in a distributed computing environment
15/245,768	US	8/24/2016	
15/716,279	US	9/26/2017	Distributed computing
			SYSTEM CLOCK DISTRIBUTION IN A DISTRIBUTED COMPUTING ENVIRONMENT
16/224,639	US	12/18/2018	
			Serially connected computing nodes in a distributed computing system
16/542,192	US	8/15/2019	