PATENT ASSIGNMENT COVER SHEET

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NATURE OF CONVEYANCE:	ASSIGNMENT

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Name	Execution Date
INTELLECTUAL VENTURES ASSETS 157 LLC	12/30/2019

RECEIVING PARTY DATA

Name:	LIBERTY PATENTS LLC
Street Address:	2325 OAK ALLEY
City:	TYLER
State/Country:	TEXAS
Postal Code:	75703

PROPERTY NUMBERS Total: 15

Property Type	Number
Patent Number:	6894616
Patent Number:	6826587
Patent Number:	6667513
Patent Number:	6551872
Patent Number:	6577987
Patent Number:	6762124
Patent Number:	7216345
Patent Number:	RE42449
Application Number:	12420799
Application Number:	60122817
Application Number:	12104331
PCT Number:	US2002004290
PCT Number:	US2000005705
PCT Number:	US2001019307
PCT Number:	US2001019273

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Total Attachments: 6
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Exhibit B-1

ASSIGNMENT OF RIGHTS IN CERTAIN ASSETS

For good and valuable consideration, the receipt of which is hereby acknowledged, Intellectual Ventures Assets 157 LLC, a Delaware limited liability company, with an address at 251 Little Falls Drive, Wilmington, DE 19808 ("***Assignor***"), does hereby sell, assign, transfer, and convey unto Liberty Patents LLC, a Texas limited liability company with an address at 2325 Oak Alley, Tyler, Texas 75703 ("***Assignee***"), its right, title, and interest in and to any and all of the following provisional patent applications, patent applications, patents, and other governmental grants or issuances of any kind (the "***Certain Assets***"):

Patent/Application Number	**Country**	**Issue Date/ Filing Date**	**Title Of Patent And First Named Inventor**
(GB9917856.8)	GB	(7/29/1999)	Piezoelectric Tag Forster, Ian James
(PCT/GB2000/002944)	WO	(7/31/2000)	Piezo-Electric Tag Forster, Ian James
AU781231 (AU63018/00)	AU	5/12/2005 (7/31/2000)	Piezo-Electric Tag Forster, Ian James
(AU2005203609)	AU	(8/12/2005)	Piezo-Electric Tag Forster, Ian James
(CA2380534)	CA	(7/31/2000)	Piezo-Electric Tag Forster, Ian James
EP1206709 (EP00949745.4)	EP	6/7/2006 (7/31/2000)	Piezo-Electric Tag Forster, Ian James
EP1691426 (EP06011154.9)	EP	4/30/2014 (7/31/2000)	Piezo-Electric Transducers Forster, Ian James
6894616 (10/048108)	US	5/17/2005 (7/31/2000)	Piezo-Electric Tag Forster, Ian James
FR2793971 (FR9906425)	FR	8/31/2001 (5/20/1999)	Complex Number Multiplier Arndt Marylin

Patent/Application Number	Country	Issue Date/ Filing Date	Title Of Patent And First Named Inventor
IT1190344 (IT00929626.0)	IT	7/30/2003 (5/18/2000)	Complex Number Multiplier Arndt Marylin
6826587 (09/979283)	US	11/30/2004 (11/20/2001)	Complex Number Multiplier Luis Montalvo
FR2794898 (FR9907391)	FR	9/14/2001 (6/11/1999)	Semiconductor Device With Compensated Threshold Voltage And Method For Making Same Gwoziecki Romain
6667513 (10/018179)	US	12/23/2003 (5/3/2002)	Semiconductor Device With Compensated Threshold Voltage And Method For Making Same Thomas Skotnicki
FR2789804 (FR9901925)	FR	8/23/2002 (2/17/1999)	Anisotropic Plasma Etching Of A Dielectric, Organic Polymer Material Joubert, Olivier
(JP2000-030132)	JP	(2/8/2000)	Anisotropic Etching Method For Organic Dielectric Polymer Material By Plasma Gas Joubert, Olivier
(EP00400363.8)	EP	(2/9/2000)	Anisotropic Plasma Etching Of A Dielectric, Organic Polymer Material Joubert, Olivier
6551872 (09/642140)	US	4/22/2003 (8/18/2000)	Method For Making Integrated Circuit Including Interconnects With Enhanced Electromigration Resistance Using Doped Seed Layer And Integrated Circuits Produced Thereby James A. Cunningham
6577987 (09/715019)	US	6/10/2003 (11/20/2000)	Operational Monitoring For A Converter Wenning, Guido
6762124 (09/783122)	US	7/13/2004 (2/14/2001)	Method For Patterning A Multilayered Conductor/Substrate Structure

Patent/Application Number	Country	Issue Date/ Filing Date	Title Of Patent And First Named Inventor
			Kouroche Kian
(PCT/US2002/004290)	WO	(2/13/2002)	Method For Patterning A Multilayered Conductor/Substrate Structure Kouroche Kian
GB2389562 (GB0319660.7)	GB	8/14/2004 (2/13/2002)	Method For Patterning A Multilayered Conductor/Substrate Structure Kouroche Kian
(12/420799)	US	(4/8/2009)	Method For Patterning A Multilayered Conductor/Substrate Structure Kian, Kouroche
TWI170084 (TW090116761)	TW	1/1/2003 (7/9/2001)	Bi-Directional Low-Pass Filter For Use At User End Or Office End In Communication Netpctrk Her-Jun Chueh
TWI548906 (TW090116761A01)	TW	8/21/2003 (8/10/2001)	Bi-Directional Low-Pass Filter For Use At User End Or Office End In Communication Netpctrk Her-Jun Chueh
CNZL02106525.X (CN02106525.X)	CN	7/27/2005 (2/26/2002)	Bi-Directional Low-Pass Filter For Use At User End Or Office End In Communication Network Her-Jun Chueh
(60/122817)	US	(3/4/1999)	Mpeg-Like Data Represented As Analog Video Robert Norman Hurst Jr.
(PCT/US2000/005705)	WO	(3/4/2000)	Method And Apparatus For Carrying Digital Data As Analog Video Hurst Robert Norman Jr
(PCT/CA2001/000712)	WO	(5/22/2001)	Processor Array And Parallel Data Processing Methods Strayer Jayson D
(IN1281/DEL/2002)	IN	(12/19/2002)	High Performance Interconnect Architecture For Fpgas

Patent/Application Number	Country	Issue Date/ Filing Date	Title Of Patent And First Named Inventor
			Bal, Ankur
(EP03104727.7)	EP	(12/16/2003)	High Performance Interconnect Architecture For Fpgas Bal Ankur
(12/104331)	US	(4/16/2008)	High Performance Interconnect Architecture For Field Programmable Gate Arrays Bal, Ankur
DE19947032.4 (DE19947032.4)	DE	2/23/2006 (9/30/1999)	System For Controlling And Monitoring Telecommunication Terminals Connected To A Private Branch Exchange (PBX) Or A Wide Area Network (WAN) Langer Uwe
EP1089529 (EP00120876.8)	EP	11/23/2005 (9/25/2000)	System For Controlling And Monitoring Telecommunication Terminals Connected To A Private Branch Exchange (PBX) Or A Wide Area Network (WAN) Schindel Frank
DE50011678.4 (DE50011678.4)	DE	11/23/2005 (9/25/2000)	System For Controlling And Monitoring Telecommunication Terminals Connected To A Private Branch Exchange (PBX) Or A Wide Area Network (WAN) Schindel Frank
FR1089529 (FR00120876.8)	FR	11/23/2005 (9/25/2000)	System For Controlling And Monitoring Telecommunication Terminals Connected To A Private Branch Exchange (PBX) Or A Wide Area Network (WAN) Schindel Frank
GB1089529 (GB00120876.8)	GB	11/23/2005 (9/25/2000)	System For Controlling And Monitoring Telecommunication Terminals Connected To A Private Branch Exchange (PBX) Or A Wide Area Network (WAN) Schindel Frank

Patent/Application Number	Country	Issue Date/ Filing Date	Title Of Patent And First Named Inventor
IT1089529 (IT00120876.8)	IT	11/23/2005 (9/25/2000)	System For Controlling And Monitoring Telecommunication Terminals Connected To A Private Branch Exchange (PBX) Or A Wide Area Network (WAN) Schindel Frank
(EP01106814.5)	EP	(3/19/2001)	Apparatus, Method And System For Synchronizing Slave System Operations To Master System Clocking Signals In A Master-Slave Asynchronous Communication System Matthias Reinert
(DE10047592.2)	DE	(9/26/2000)	Ventilation Device With Electromagnetic Coupling Ekkehard Pittius
(PCT/DE2001/003654)	WO	(9/21/2001)	Ventilation Device With Electromagnetic Coupling Ekkehard Pittius
EP1323224 (EP01978180.6)	EP	12/20/2006 (9/21/2001)	Ventilation Device With Electromagnetic Coupling Ekkehard Pittius
FR1323224 (FR01978180.6)	FR	12/20/2006 (9/21/2001)	Ventilation Device With Electromagnetic Coupling Ekkehard Pittius
(PCT/US2001/019307)	WO	(6/15/2001)	Floating Gate Transistor Having Buried Strained Silicon Germanium Channel Layer Kencke David L
(PCT/US2001/019273)	WO	(6/15/2001)	Vertical Channel Floating Gate Transistor Having Silicon Germanium Channel Layer Kencke David L
7216345 (09/544492)	US	5/8/2007 (4/7/2000)	Method And Apparatus For Protectively Operating A Data/Information Processing Device Swain W. Porter

Patent/Application Number	Country	Issue Date/ Filing Date	Title Of Patent And First Named Inventor
KR10-0712327 (KR10-2002-7001252)	KR	4/20/2007 (7/31/2000)	Piezo-Electric Tag Forster, Ian James
RE42449 (11/804715)	US	6/14/2011 (5/17/2007)	Piezo-Electric Tag Forster, Ian James

Assignor assigns to Assignee all of its rights to the inventions, invention disclosures, and discoveries in the assets listed above, together, with its rights, if any, to revive prosecution of claims under such assets and to sue or otherwise enforce any claims under such assets for past, present or future infringement.

Assignor hereby authorizes the respective patent office or governmental agency in each jurisdiction to make available to Assignee all records regarding the Certain Assets.

The terms and conditions of this Assignment of Rights in Certain Assets will inure to the benefit of Assignee, its successors, assigns, and other legal representatives and will be binding upon Assignor, its successors, assigns, and other legal representatives.

EXECUTED this 30th day of December 2019, to be effective as of December 30, 2019.

ASSIGNOR:

INTELLECTUAL VENTURES ASSETS 157 LLC

By: Jim Weisfield
Jim Weisfield (Dec 30, 2019)
Name: Jim Weisfield
Title: Authorized Person