

10-20-2000

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FORM PTO-1595

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U.S. DEPARTMENT OF COMMERCE
Patent and Trademark Office

To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):
Silicon Valley Bank
3003 Tasman Dr. HG150
Santa Clara, Ca. 95054

Additional name(s) of conveying party(ies) attached? Yes No

2. Name and address of receiving party(ies):
Name: **Kota Microcircuits, Inc.**
Internal Address:

3. Nature of conveyance:
 Assignment Merger
 Security Agreement Change of Name
 Other _ Release _____

Execution Date: September 14, 2000

Street Address: **1215 South Grant Avenue**
City: **Loveland** State: **CO** ZIP: **80537-6399**

Additional name(s) & address(es) attached? Yes No

4. Application number(s) or trademark number(s):
If this document is being filed together with a new application, the execution date of the application is:

A. Trademark Application No.(s)
1092530

Additional numbers attached? Yes No

B. Trademark No.(s)

5. Name and address of party to whom correspondence concerning document should be mailed:
Name: **Silicon Valley Bank**
Internal Address: **Loan Documentation HG150**

Street Address: **3003 Tasman Dr.**
City: **Santa Clara** State: **Ca** ZIP: **95054**

6. Total number of applications and trademark involved:

7. Total fee (37 CFR 3.41): **\$40.00**
 Enclosed
 Authorized to be charged to deposit account

8. Deposit account number:
(Attach duplicate copy of this page if paying by deposit account)

DO NOT USE THIS SPACE

9. Statement and signature.
To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Simone Reeves
Name of Person Signing

Simone Reeves
Signature

Total number of pages comprising cover sheet: **2**

Date: **10/18/2000**

OMB No 0651 -0011 (exp 4/94)

TRADEMARK
REEL: 002157 FRAME: 0839


10/18/2000 DBYRNE 00000084 1092530 40.00 DP 01 FC:461

**RELEASE OF SECURITY AGREEMENT COVERING
INTERESTS IN TRADEMARKS**

Silicon Valley Bank ("Secured Party"), hereby releases its security interest in the interests of **KOTA Microcircuits, Inc.** ("Assignor") in the trademarked works set forth in that certain **Intellectual Property Security Agreement** dated **August 25, 1998**, executed by Assignor in favor of Secured Party recorded with the United States Department of Commerce, Patent and Trademark Office on September 18, 1998, Reel 1796, Frame(s) 0972.

Dated: **September 14, 2000**

SILICON VALLEY BANK

By: 
Name: AMY B. YOUNG
Title: VICE PRESIDENT

INTELLECTUAL PROPERTY SECURITY AGREEMENT

This Intellectual Property Security Agreement is entered into as of July 27, 2000 by and between SILICON VALLEY BANK ("Bank") and Stream Machine Company ("Grantor").

RECITALS

A. Bank has agreed to make certain advances of money and to extend certain financial accommodation to Grantor (the "Loans") in the amounts and manner set forth in that certain Loan and Security Agreement by and between Bank and Grantor dated September 8, 1999 (as the same may be amended, modified or supplemented from time to time, the "Loan Agreement"; capitalized terms used herein are used as defined in the Loan Agreement). Bank is willing to make the Loans to Grantor, but only upon the condition, among others, that Grantor shall grant to Bank a security interest in certain Copyrights, Trademarks, Patents, and Mask Works to secure the obligations of Grantor under the Loan Agreement.

B. Pursuant to the terms of the Loan Agreement, Grantor has granted to Bank a security interest in all of Grantor's right, title and interest, whether presently existing or hereafter acquired, in, to and under all of the Collateral.

NOW, THEREFORE, for good and valuable consideration, receipt of which is hereby acknowledged, and intending to be legally bound, as collateral security for the prompt and complete payment when due of its obligations under the Loan Agreement, Grantor hereby represents, warrants, covenants and agrees as follows:

AGREEMENT

To secure its obligations under the Loan Agreement, Grantor grants and pledges to Bank a security interest in all of Grantor's right, title and interest in, to and under its Intellectual Property Collateral (including without limitation those Copyrights, Patents, Trademarks and Mask Works listed on Schedules A, B, C, and D hereto), and including without limitation all proceeds thereof (such as, by way of example but not by way of limitation, license royalties and proceeds of infringement suits), the right to sue for past, present and future infringements, all rights corresponding thereto throughout the world and all re-issues, divisions continuations, renewals, extensions and continuations-in-part thereof.

This security interest is granted in conjunction with the security interest granted to Bank under the Loan Agreement. The rights and remedies of Bank with respect to the security interest granted hereby are in addition to those set forth in the Loan Agreement and the other Loan Documents, and those which are now or hereafter available to Bank as a matter of law or equity. Each right, power and remedy of Bank provided for herein or in the Loan Agreement or any of the Loan Documents, or now or hereafter existing at law or in equity shall be cumulative and concurrent and shall be in addition to every right, power or remedy provided for herein and the exercise by Bank of any one or more of the rights, powers or remedies provided for in this Intellectual Property Security Agreement, the Loan Agreement or any of the other Loan Documents, or now or hereafter existing at law or in equity, shall not preclude the simultaneous or later exercise by any person, including Bank, of any or all other rights, powers or remedies.

IN WITNESS WHEREOF, the parties have cause this Intellectual Property Security Agreement to be duly executed by its officers thereunto duly authorized as of the first date written above.

Address of Grantor:

2345 N. First Street, Suite 200
San Jose, CA 95131

Attn: Mukund Marathe


Address of Bank:

3003 Tasman Drive
Santa Clara, CA 94054

Attn: _____

GRANTOR:

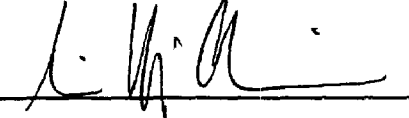
Stream Machine Company

By: 

Title: Vice President & CFO

BANK:

SILICON VALLEY BANK

By: 

Title: VP

EXHIBIT B

Patents

Description

Registration/
Application
Number

Registration/
Application
Date

- | <u>Description</u> | <u>Registration/
Application
Number</u> | <u>Registration/
Application
Date</u> |
|--|---|---|
| ① Multistage and low-complexity Motion Estimation for Inter frame video encoding | 08/896,618 | July 18, 1997 |
| ② Single-pass variable bit-rate control for digital video encoding | | Aug-21, 1997 |
| ③ Variable-size spatial and temporal video scaler | | Oct 1, 1997 |
| ④ LSI Architecture and implementation of MPEG video codec | | July 17, 1998 |
| ⑤ Postprocessing system for remaining blocking artifacts in block-based codecs | | July 8, 1998 |
| ⑥ Fast implementation of MPEG Audio coding | | June 5, 2000 |
| ⑦ A versatile video interface architecture for video compression & decompression | | June 28, 2000 |