

**TRADEMARK ASSIGNMENT**

Electronic Version v1.1  
 Stylesheet Version v1.1

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT		
<b>NATURE OF CONVEYANCE:</b>	SECURITY INTEREST		
<b>CONVEYING PARTY DATA</b>			
<b>Name</b>	<b>Formerly</b>	<b>Execution Date</b>	<b>Entity Type</b>
SEQUENCE DESIGN, INC.		11/24/2004	CORPORATION: CALIFORNIA

<b>RECEIVING PARTY DATA</b>	
<b>Name:</b>	SILICON VALLEY BANK
<b>Street Address:</b>	3003 TASMAN DRIVE
<b>City:</b>	SANTA CLARA
<b>State/Country:</b>	CALIFORNIA
<b>Postal Code:</b>	95054
<b>Entity Type:</b>	CHARTERED BANK: CALIFORNIA

**PROPERTY NUMBERS Total: 14**

Property Type	Number	Word Mark
Serial Number:	78590851	COOL BY DESIGN
Serial Number:	78590841	COOLTHEATRE
Serial Number:	78590865	COOLROUTE
Serial Number:	78822255	COOLCHECK
Serial Number:	78727932	NANOCOOL
Registration Number:	3225732	COOLPOWER
Serial Number:	77395049	WHAT COLOR IS YOUR RTL?
Serial Number:	77469301	POWERCANVAS
Serial Number:	77343496	VECTOR SELECTOR
Serial Number:	77372416	POWERARTIST
Serial Number:	77372263	IT'S COOL TO BE GREEN
Serial Number:	77338082	SEQUENCE DESIGN
Serial Number:	77213573	DESIGN FOR POWER
Serial Number:	77213536	SMARTSOURCE

**OP \$365.00 78590851**

CORRESPONDENCE DATA

Fax Number: (404)962-6736  
*Correspondence will be sent via US Mail when the fax attempt is unsuccessful.*  
Phone: (404) 885-3038  
Email: michael.brignati@troutmansanders.com  
Correspondent Name: MICHAEL J. BRIGNATI, PH.D.  
Address Line 1: TROUTMAN SANDERS LLP  
Address Line 2: 600 PEACHTREE STREET, N.E.  
Address Line 4: ATLANTA, GEORGIA 30308-2216

ATTORNEY DOCKET NUMBER:	220763.000443
NAME OF SUBMITTER:	Michael J. Brignati, Ph.D.
Signature:	/Michael J. Brignati 60,890/
Date:	07/08/2008

Total Attachments: 7  
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## **INTELLECTUAL PROPERTY SECURITY AGREEMENT**

THIS INTELLECTUAL PROPERTY SECURITY AGREEMENT is entered into as of November 24, 2004 by and between SILICON VALLEY BANK ("Bank") and SEQUENCE DESIGN, INC. ("Grantor").

### **RECITALS**

A. Bank has agreed to make certain advances of money and to extend certain financial accommodation to Grantor (the "Loans") in the amounts and manner set forth in that certain Second Amended and Restated Loan and Security Agreement by and between Bank and Grantor and the other Borrowers party thereto of even date herewith (as the same may be amended, modified or supplemented from time to time, the "Loan Agreement"; capitalized terms used herein are used as defined in the Loan Agreement). Bank is willing to make the Loans to Grantor, but only upon the condition, among others, that Grantor shall grant to Bank a security interest in certain Copyrights, Trademarks, Patents, and Mask Works to secure the obligations of Grantor under the Loan Agreement.

B. Pursuant to the terms of the Loan Agreement, Grantor has granted to Bank a security interest in all of Grantor's right, title and interest, whether presently existing or hereafter acquired, in, to and under all of the Collateral.

NOW, THEREFORE, for good and valuable consideration, receipt of which is hereby acknowledged, and intending to be legally bound, as collateral security for the prompt and complete payment when due of its obligations under the Loan Agreement, Grantor hereby represents, warrants, covenants and agrees as follows:

### **AGREEMENT**

To secure its obligations under the Loan Agreement, Grantor grants and pledges to Bank a security interest in all of Grantor's right, title and interest in, to and under its Intellectual Property Collateral (including without limitation those Copyrights, Patents and Trademarks and Mask Works listed on Exhibits A, B and C and D hereto), and including without limitation all proceeds thereof (such as, by way of example but not by way of limitation, license royalties and proceeds of infringement suits), the right to sue for past, present and future infringements, all rights corresponding thereto throughout the world and all re-issues, divisions continuations, renewals, extensions and continuations-in-part thereof.

This security interest is granted in conjunction with the security interest granted to Bank under the Loan Agreement. The rights and remedies of Bank with respect to the security interest granted hereby are in addition to those set forth in the Loan Agreement and the other Loan Documents, and those which are now or hereafter available to Bank as a matter of law or equity. Each right, power and remedy of Bank provided for herein or in the Loan Agreement or any of the Loan Documents, or now or hereafter existing at law or in equity shall be cumulative and concurrent and shall be in addition to every right, power or remedy provided for herein and the exercise by Bank of any one or more of the rights, powers or remedies provided for in this

Intellectual Property Security Agreement, the Loan Agreement or any of the other Loan Documents, or now or hereafter existing at law or in equity, shall not preclude the simultaneous or later exercise by any person, including Bank, of any or all other rights, powers or remedies.

***[SIGNATURES APPEAR ON THE FOLLOWING PAGE]***


IN WITNESS WHEREOF, the parties have caused this Intellectual Property Security Agreement to be duly executed by its officers thereunto duly authorized as of the first date written above.

**GRANTOR:**

Address of Grantor:

469 El Camino Real, Suite 202  
Santa Clara, California 95050

SEQUENCE DESIGN, INC.

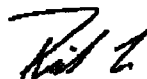
By:   
Name: VIC KULKARNI  
Title: President & CEO

**BANK:**

Address of Bank:

3003 Tasman Drive  
Santa Clara, California 95054

SILICON VALLEY BANK

By:   
Name: Rick Tu  
Title: VP

**EXHIBIT A**

Copyrights

<u>Description</u>	<u>Registration/ Application Number</u>	<u>Registration/ Application Date</u>
NONE	N/A	N/A

**EXHIBIT B**

**Patents**

<u>Description</u>	<u>Registration/ Application Number</u>	<u>Registration/ Application Date</u>
Automatic extension of clock gating technique to fine-grained power gating	7,323,909	01/29/2008
Circuit optimization for minimum path timing violations	7,222,318	05/22/2007
Method for determining load capacitance	7,003,741	02/21/2006
Method and apparatus for logic synthesis with elaboration	6,519,755	02/11/2003
Automatic voltage drop optimization	11/551,149	10/19/2006
Design method and architecture for power gate switch placement and interconnection using tapless libraries	11/331,913	01/13/2006
Automatic extension of clock gating technique to fine-grained power gating	11/193,149	07/29/2005
Design method and architecture for power gate switch placement	10/998,204	11/26/2004
Method that allows flexible evaluation of power-gated circuits	11/150,031	06/10/2005
Automatic extension of clock gating technique to fine-grained power gating	11/952,937	12/07/2007

**EXHIBIT C**

Trademarks

<u>Description</u>	<u>Registration/ Application Number</u>	<u>Registration/ Application Date</u>
COOLGRID (Abandoned)	78/590,881	03/18/2005
COOLPLACE (Abandoned)	78/590,858	03/18/2005
COOL BY DESIGN (Pending)	78/590,851	03/18/2005
COOLTHEATRE (Pending)	78/590,841	03/18/2005
COOLROUTE (Pending)	78/590,865	03/18/2005
COOLCHECK (Pending)	78/822,255	02/23/2006
NANOCOOL (Pending)	78/727,932	10/06/2005
COOLPOWER (Registered)	3,225,732	04/03/2007
ELECTRICALLYCORRECT (Abandoned)	78/066,585	05/31/2001
FULLCONTEXT (Abandoned)	78/066,579	05/31/2001
WHAT COLOR IS YOUR RTL? (Pending)	77/395,049	02/12/2008
POWERCANVAS (Pending)	77/469,301	05/08/2008
VECTOR SELECTOR (Pending)	77/343,496	12/04/2007
POWERARTIST (Pending)	77/372,416	01/15/2008
IT'S COOL TO BE GREEN (Pending)	77/372,263	01/15/2008
SEQUENCE DESIGN (Pending)	77/338,082	11/27/2007
DESIGN FOR POWER (Pending)	77/213,573	06/22/2007
SMARTSOURCE (Pending)	77/213,536	06/22/2007
SEQUENCE (Abandoned)	74/326,588	10/28/1992



**EXHIBIT D**

**Mask Works**

**Description**

**Registration/  
Application  
Number**

**Registration/  
Application  
Date**

NONE

N/A

N/A