

TRADEMARK ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	SECURITY INTEREST

CONVEYING PARTY DATA

Name	Formerly	Execution Date	Entity Type
Apex Microtechnology, Inc.		08/17/2012	CORPORATION: ARIZONA

RECEIVING PARTY DATA

Name:	Fidus Mezzanine Capital, L.P.
Street Address:	1603 Orrington Avenue
Internal Address:	Suite 820
City:	Evanston
State/Country:	ILLINOIS
Postal Code:	60201
Entity Type:	LIMITED PARTNERSHIP: DELAWARE

PROPERTY NUMBERS Total: 7

Property Type	Number	Word Mark
Registration Number:	2158606	APEX MICROTECHNOLOGY
Registration Number:	2158607	APEX μ TECH
Registration Number:	2372020	APEX MICROTECHNOLOGY
Registration Number:	3287891	PRECISION ICS
Registration Number:	3662385	APEX PRECISION POWER
Registration Number:	3608129	APEX PRECISION POWER
Registration Number:	4055016	APEX

CORRESPONDENCE DATA

Fax Number: 6027985595
Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent via US Mail.

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 Correspondent Name: Ballard Spahr LLP, Kurt C. Donnell

CH \$190.00 2158606

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ATTORNEY DOCKET NUMBER: 00138984

NAME OF SUBMITTER: Kurt C. Donnell

Signature: /Kurt C. Donnell/

Date: 08/17/2012

Total Attachments: 3

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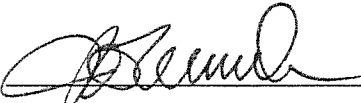
**GRANT OF SECURITY INTEREST
IN PATENTS AND TRADEMARKS**

WHEREAS, APEX MICROTECHNOLOGY, INC. (the "Pledgor") is the owner of the trademark applications and registrations listed on Schedule A attached hereto, (all such trademarks, registrations and applications, collectively, the "Trademarks") and is the owner of the patents and patent applications listed on Schedule A attached hereto (all such patents, registrations and applications, collectively, the "Patents"); and

WHEREAS, the Pledgor has entered into a Pledge and Security Agreement (as amended, modified, restated or supplemented from time to time, the "Security Agreement"), dated as of August 17, 2012, in which the Pledgor has agreed with Fidus Mezzanine Capital, L.P., as Collateral Agent (the "Collateral Agent"), with offices at 1603 Orrington Avenue, Suite 820, Evanston, IL 60201, to execute this Grant;

NOW, THEREFORE, for good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, as security for the payment and performance of the Secured Obligations (as defined in the Security Agreement), the Pledgor does hereby grant to the Collateral Agent a security interest in all of its right, title and interest in and to the Trademarks and the Patents, and the use thereof, together with all proceeds and products thereof and the goodwill of the businesses symbolized by the Trademarks. This Grant has been given in conjunction with the security interest granted to the Collateral Agent under the Security Agreement, and the provisions of this Grant are without prejudice to and in addition to the provisions of the Security Agreement, which are incorporated herein by this reference.

APEX MICROTECHNOLOGY, INC.

By: 
Title: President

Schedule A

TRADEMARKS AND TRADEMARK APPLICATIONS

<u>Owner</u>	<u>Mark</u>	<u>Application or Registration No.</u>	<u>Country</u>	<u>Registration Date</u>
<u>Apex Microtechnology, Inc.</u>	<u>APEX MICROTECHNOLOGY in Words, Letters or Numbers in Typed Form in Class 9</u>	No. 2,158,606	<u>U.S.</u>	5/19/1998
<u>Apex Microtechnology, Inc.</u>	<u>APEX μTECH in Stylized Words, Letters or Numbers in Class 9</u>	No. 2,158,607	<u>U.S.</u>	5/19/1998
<u>Apex Microtechnology, Inc.</u>	<u>APEX MICROTECHNOLOGY in Stylized Words, Letters or Numbers in Class 9</u>	No. 2,372,020	<u>U.S.</u>	8/1/2000
<u>Apex Microtechnology, Inc.</u>	<u>PRECISION IC's in Words, Letters or Numbers in Typed Form in Class 9</u>	No. 3,287,891	<u>U.S.</u>	9/4/2007
<u>Apex Microtechnology, Inc.</u>	<u>APEX PRECISION POWER in Words, Letters or Numbers in Typed Form in Class 9</u>	No. 3,662,385	<u>U.S.</u>	4/4/2009
<u>Apex Microtechnology, Inc.</u>	<u>APEX PRECISION POWER in Stylized Words, Letters or Numbers in Class 9</u>	No. 3,608,129	<u>U.S.</u>	4/21/2009
<u>Apex Microtechnology, Inc.</u>	<u>APEX in Words, Letters or Numbers in Typed Form in Class 9</u>	No. 4,055,016	<u>U.S.</u>	11/15/2010

Schedule A

PATENTS AND PATENT APPLICATIONS

CaseNumber	Country	CaseType	ApplicationStatus	PatNumber	IssDate	AppNumber	FillDate	InvTitle
1771	US	ORD	Expired	4,871,965	03-Oct-1989	07/235,751	18-Aug-1988	Environmental Testing Facility for Electronic Components
1772	US	ORD	Granted	5,142,243	25-Aug-1992	07/618,388	19-Dec-1990	Apparatus and Method for an Input Stage of an Operational Amplifier
1772	US	CIP	Granted	5,210,505	11-May-1993	04/848,863	10-Mar-1992	Apparatus and Method for an Input Stage of an Operational Amplifier
1773	US	ORD	Granted	5,365,194	15-Nov-1994	08/072,329	03-Jun-1993	Power Operational Amplifier Having Oscillation Limiter and Method
1774	US	ORD	Granted	5,519,357	21-May-1996	08/391,353	21-Feb-1995	Biasing Arrangement for a Quasi-Complementary Output Stage
1775	US	ORD	Granted	6,496,068	17-Dec-2002	09/712,733	14-Nov-2000	Multi-Stage Amplifier Circuit
1775	WO	ORD	Expired			US2000/031288	14-Nov-2000	Multi-Stage Amplifier Circuit
1749	US	ORD	Closed					Complimentary Defined Ratio Current Source Exponential DAC
1750	US	ORD	Granted	7,786,799	31-Aug-2010	12/053,534	21-Mar-2008	Trimming Technique for High Voltage Amplifiers Using Floating Low Voltage Structures
1752	US	RCE	Published			11/941,023	15-Nov-2007	Inverted Through Circuit Board Mounting with Heat Sink
1752	CN	ORD	Granted	2008101814656	04-Jul-2011	200810181465.6	14-Nov-2008	Inverted Through Circuit Board Mounting with Heat Sink
1762	US	ORD	Granted	7,292,087	06-Nov-2007	11/378,014	15-Mar-2006	Power Supplies for Driving H-Bridges
1762	US	CON	Granted	7,477,088	13-Jan-2009	11/935,227	05-Nov-2007	Power Supplies for Driving H-Bridges
1763	US	ORD	Granted	7,619,476	17-Nov-2009	11/378,029	15-Mar-2006	Biasing Stage for an Amplifier
1764	US	ORD	Granted	7,339,433	04-Mar-2008	11/389,574	15-Mar-2006	Differential Amplifier Stage
1769	US	ORD	Expired	4,808,909	28-Feb-1989	07/108,745	15-Oct-1987	Bias Voltage and Constant Current Supply Circuit
1770	US	ORD	Expired	4,833,423	23-May-1989	07/075,084	20-Jul-1987	Apparatus and Method for a Wire-Band Direct-Coupled Transistor

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